

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_J = 25^\circ\text{C}$. $V_{DD33} = 3.3\text{V}$, $V_{IN_SNS} = 12\text{V}$, V_{DD25} and REF pins floating, unless otherwise indicated. (Notes 2, 3)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
f_{IN_ADC}	Input Sampling Frequency			62.5			kHz
I_{IN_ADC}	Input Leakage Current	$V_{IN_ADC} = 0\text{V}$, $0\text{V} \leq V_{COMMONMODE} \leq 6\text{V}$, Current Sense Mode		●		± 0.5	μA
	Differential Input Current	$V_{IN_ADC} = 0.17\text{V}$, Current Sense Mode		●	80	250	nA
		$V_{IN_ADC} = 6\text{V}$, Voltage Sense Mode		●	10	15	μA
DAC Output Characteristics							
N_{VDACP}	Resolution			1.29			
V_{FS_VDACP}	Full-Scale Output Voltage (Programmable)	$V_{DACP} = 0x3FF$	Buffer Gain Setting_0	●	1.32	1.38	1.44
		$V_{DACP} = 0x000$	Buffer Gain Setting_1	●	2.50	2.65	2.77
INL_{VDACP}	Integral Nonlinearity	(Note 8)		✗	2.48	± 2	LSB
DNL_{VDACP}	Differential Nonlinearity	(Note 8)		●		± 2.4	LSB
V_{OS_VDACP}	Offset Voltage	(Note 8)		●		± 20	mV
V_{DACP}	Load Regulation ($V_{DACPn} - V_{DACPm}$)		$V_{DACPn} = 2.65\text{V}$, I_{VDACPn} Sourcing = 2mA		100		ppm/mA
			$V_{DACPn} = 0.1\text{V}$, I_{VDACPn} Sinking = 2mA		100		ppm/mA
	PSRR ($V_{DACPn} - V_{DACPm}$)		DC: $3.13\text{V} \leq V_{DD33} \leq 3.47\text{V}$		60		dB
			100mV Step in 20ns with 50pF Load		40		dB
	DC CMRR ($V_{DACPn} - V_{DACPm}$)		$-0.1\text{V} \leq V_{DACPm} \leq 0.1\text{V}$		60		dB
	Leakage Current		V_{DACPn} Hi-Z, $0\text{V} \leq V_{DACPn} \leq 6\text{V}$		●	± 100	nA
	Short-Circuit Current Low		V_{DACPn} Shorted to GND		●	-10	-4
	Short-Circuit Current High		V_{DACPn} Shorted to V_{DD33}		●	4	10
C_{OUT}	Output Capacitance	V_{DACPn} Hi-Z			10		pF
t_{S_VDACP}	DAC Output Update Rate	Fast Servo Mode			500		μs
DAC Soft-Connect Comparator Characteristics							
V_{OS_CMP}	Offset Voltage	$V_{DACPn} = 0.2\text{V}$		●	± 1	± 18	mV
		$V_{DACPn} = 1.3\text{V}$		●	± 2	± 26	mV
		$V_{DACPn} = 2.65\text{V}$		●	± 3	± 52	mV
Voltage Supervisor Characteristics							
V_{IN_VS}	Input Voltage Range (Programmable)	$V_{IN_VS} = (V_{SENSEPn} - V_{SENSEMn})$	Low Resolution Mode	●	0	6	V
			High Resolution Mode	●	0	3.8	V
		Single-Ended Voltage: $V_{SENSEMn}$		●	-0.1	0.1	V
N_{VS}	Voltage Sensing Resolution	0V to 3.8V Range: High Resolution Mode			4		mV/LSB
		0V to 6V Range: Low Resolution Mode			8		mV/LSB
TUE_{VS}	Total Unadjusted Error	$2\text{V} \leq V_{IN_VS} \leq 6\text{V}$, Low Resolution Mode		●		± 1.25	% of Reading
		$1.5\text{V} < V_{IN_VS} \leq 3.8\text{V}$, High Resolution Mode		●		± 1.0	% of Reading
		$0.8\text{V} \leq V_{IN_VS} \leq 1.5\text{V}$, High Resolution Mode		●		± 1.5	% of Reading
t_{S_VS}	Update Period				12.21		μs