

**Design Name** //MAXSJHFS03/Modules/Hardware\_Design/Schematics/Orcad\_16.2\_Allegro/Stella/Stella3\_Mod1\_EVK3/Allegro/35-900356-01-00/35-900356-01-00.brd  
**Date** Fri Jan 23 10:58:36 2015

**DRC Error Count Summary**

DRC Error Type	DRC Error Count
Package to Package Keepin	3
Package to Package	4
Total DRC Errors	7

**Detailed DRC Errors**

Constraint Name	DRC Marker Location	Required Value	Actual Value	Constraint Source	Constraint Source Type	Element 1	Element 2	Comment
Package to Place Keepin Spacing	(20.0 2563.5)	0 MIL	0 MIL	NONE	DESIGN	Filled Rectangle "J1, Package Geometry/Place_Bound_Top"	Rectangle "Package Keepin/All"	approved for fab
Package to Place Keepin Spacing	(2417.0 20.0)	0 MIL	0 MIL	NONE	DESIGN	Shape "J3, Package Geometry/Place_Bound_Top"	Rectangle "Package Keepin/All"	approved for fab
Package to Place Keepin Spacing	(2828.0 3225.0)	0 MIL	0 MIL	NONE	DESIGN	Shape "J702, Package Geometry/Place_Bound_Top"	Rectangle "Package Keepin/All"	approved for fab
Package to Package Spacing	(2703.0 1788.0)	0 MIL	44 MIL	NONE	DESIGN	Shape "D700, Package Geometry/Place_Bound_Bottom"	Filled Rectangle "U700, Package Geometry/Place_Bound_Bottom"	approved for fab
Package to Package Spacing	(1167.8 1371.6)	0 MIL	311 MIL	NONE	DESIGN	Filled Rectangle "L2, Package Geometry/Place_Bound_Top"	Filled Rectangle "L1, Package Geometry/Place_Bound_Top"	approved for fab
Package to Package Spacing	(1099.0 1825.0)	0 MIL	20 MIL	NONE	DESIGN	Filled Rectangle "R10, Package Geometry/Place_Bound_Top"	Filled Rectangle "L1, Package Geometry/Place_Bound_Top"	Approved for Fabrication
Package to Package Spacing	(1529.0 1796.0)	0 MIL	20 MIL	NONE	DESIGN	Filled Rectangle "C9, Package Geometry/Place_Bound_Top"	Filled Rectangle "L1, Package Geometry/Place_Bound_Top"	approved for fab