

AD7306 Process Transfer Qualification Plan

Qualification will be performed per Analog Devices specification ADI-0012, Procedure for qualification for New or Revised Processes or Products. The Qualification Report will be available after completion, upon request.

AD7306 process		
TEST	CONDITIONS	Quantities
High Temperature Operating Life	MIL-STD-883 , <i>Method 1005</i>	Passed
Early Life Failure	MIL-STD-883 , <i>Method 1005</i>	Passed
Highly Accelerated Stress Test (HAST)*	JEDEC JESD22-A110	Passed
Temperature Cycle*	JEDEC JESD22-A104	Passed
Autoclave*	JEDEC JESD22-A102	Passed
High Temperature Storage	JEDEC JESD22-A103	Passed
Latch-Up	JEDEC Standard 78	1x6
Electrostatic Discharge <i>Human Body Model</i>	ESD Association STM5.1-2001	1x12
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	ESD Association STM5.3.1-1999	1x15

Sample size typically 77/lot

*Preconditioned Per JEDEC/IPC J-STD-020