**QP#10511: AD5748\_AD5750\_AD5750-1\_AD5750-2 Die Rev and Package Qualification**

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| QUALIFICATION PLAN# 10511 /INCOMPLETE | | | |
| Test | Specification | Sample Size | Results |
| High Temperature Operating Life (HTOL)\* | JEDEC *JESD22-A108* | **7 x 45**  **3 x 77** | **Pass**  **Pass** |
| Highly Accelerated Stress Test (HAST)\* | JEDEC*JESD22-A110* | **3 x 77**  **3 x 77** | **Pass**  **Expected in May** |
| Temperature Cycle (TC)\* | JEDEC*JESD22-A104* | **3 x 77**  **3 x 77** | **Pass**  **Expected in May** |
| Autoclave (AC)\* | JEDEC *JESD22-A102* | **3 x 77**  **3 x 77** | **Pass**  **Expected in May** |
| High Temperature Storage Life (HTSL) | JEDEC*JESD22-A103* | **1 x 45**  **1 x 45** | **Pass**  **Expected in April** |
| Solder Heat Resistance  (SHR)\* | JEDEC/IPC *J-STD-020* | **1 x 30** | **Pass** |
| Latch-Up | JEDEC *JESD78* | **3** | **Pass** |
| Electrostatic Discharge  *Human Body Model* | ESDA/JEDEC *JS-001* | **3/voltage** | **Pass \_\_3KV** |
| Electrostatic Discharge  *Field-Induced Charged Device Model* | JEDEC *JESD22-C101* | **3/voltage** | **Pass \_1.25KV** |

\*Preconditioned per JEDEC/IPC J-STD-020