PCN # 1740 DATE: October 18, 2017

EXPECTED PCN SHIP DATE: October 18, 2017



Quality Assurance 160 Rio Robles San Jose, CA 95134

www.maximintegrated.com

PROCESS CHANGE NOTICE X PRODUCT CHANGE NOTICE

MAXIM INTEGRATED HEREBY ISSUES NOTIFICATION OF CHANGE THAT MAY AFFECT THE FOLLOWING CATEGORIES:

DESIGN	WAFER FAB	ASSEMBLY	TEST	X ELEC/MECH SPECS	
AFFECTED PRODUCT:					
Ordering P/N: (See PN listing XLS in PCN ZIP file)					
CHANGE FROM: Input Offset Voltage from 27mV (max)			CHANGE TO: Input Offset Voltage to 32mV (max)		
Change Input Offset Drift from 100uV/deg.C			Change Input Offset Drift to 320uV/deg.C		
Change PSRR (Power Supply Rejection Ratio) from 46dB			Change PSRR (Power Supply Rejection Ratio) to 45dB		
JUSTIFICATION: We are modifying the Input Offset Voltage, Input Offset Drift, and Power Supply Rejection Ratio specifications					

TRACEABILITY: Maxim Integrated maintains full traceability by device marking, packaging labels and shipment documents.

for external reference mode to improve manufacturability and better maintain production schedule commitments.

Maxim Integrated's Change Notification System is designed to keep our customer base apprised of major product, manufacturing, or facility improvements.

or

Nasser Ali Chaouche

Nasser AliChaouche / PCN Coordinator

For further information, please contact either of the people listed below.

Contact your local Maxim Integrated Company Representative

Nasser AliChaouche, PCN Coordinator 408-601-5660 / pcn.coordinator@maximintegrated.com