



DS5002FP Secure Microprocessor Chip

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REVISION C5 ERRATA

The errata listed below describe situations where DS5002FP revision C5 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to fix these errata in subsequent die revisions.

This errata sheet only applies to DS5002FP revision C5 components. Revision C5 components are branded on the topside of the package with a six-digit code of the form yywwC5, where yy and ww are two-digit numbers representing the year and work-week of manufacture, respectively. To obtain an errata sheet on another DS5002FP die revision, visit the website at www.maxim-ic.com.

1. V_{IH} VALUE FOR RST PIN EXCEEDS SPECIFICATION

Description:

The Input-High Voltage (V_{IH}) minimum specification for the RST pin will be 4.2V. The data sheet indicates that the value will be 3.5V.

Work Around:

None.