



# DS80C390 Dual CAN High-Speed Microcontroller

[www.maxim-ic.com](http://www.maxim-ic.com)

## REVISION C3 ERRATA

The errata listed below describe situations where DS80C390 revision C3 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS80C390 revision C3 components. Revision C3 components are branded on the topside of the package with a six-digit code in the form yywwC3, where yy and ww are two-digit numbers representing the year and workweek of manufacture, respectively. To obtain an errata sheet on another DS80C390 die revision, visit the website at [www.maxim-ic.com/errata](http://www.maxim-ic.com/errata).

**There are no errata for this revision.**

## DOCUMENTATION ADVISORIES:

The following alerts users to important updates in the documentation for this device. These are not errata because they may reflect permanent changes to the devices. Refer to the respective document(s) for more information.

### DS80C390 Data Sheet

A note has been added clarifying that the normalize function only operates on nonzero values.

### High-Speed Microcontroller User's Guide: DS80C390 Supplement

Additional material has been added to "Addendum to Section 12" clarifying the effect of the RCLK and TCLK bits on serial port 1 operation for revision C devices. Setting either the RCLK or TCLK bit will double the respective receive or transmit baud rate of serial port 1. This condition can be easily avoided by simply calculating a new reload value for serial port 1 corresponding to a baud rate that is half of the desired value. This feature then doubles the halved baud rate, allowing serial port 1 to operate at the desired baud rate for both transmit and receive operations.