MAX32670 ERRATA SHEET

Revision B1 Errata

The errata listed below describe situations where components of this revision perform differently than expected or differently than described in the data sheet. Analog Devices may, at its own discretion, take future steps to correct these errata when the opportunity to redesign the product presents itself. Prior to that, Analog Devices has determined the following potential workarounds that customers may want to consider when addressing one of the situations described below.

This errata sheet only applies to components of this revision. These components are branded on the topside of the package with a six-digit code in the form yywwRR, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively, and RR is the revision of the component. To obtain an errata sheet on other die revisions, visit the MAX32670 product page at analog.com/MAX32670.

1) ECC DEFEATURED

Description:
This feature is no longer supported. (15363)

Workaround:
None.

2) FLASH MEMORY MAY BE CORRUPTED DURING POWER-DOWN

Description:
An insufficient decay time of the combined V_{DD} supply can corrupt the flash memory contents. (15115)

Workaround:
The decay time of the V_{DD} supply from $V_{DD\_RST(MIN)}$ to 1.47V must be greater than 100µs.

3) FLASH MEMORY MAY BE CORRUPTED DURING POWER-DOWN WHEN OPERATING IN DUAL-SUPPLY MODE

Description:
Incorrect sequencing of the V_{DD} and V_{CORE} supplies can corrupt the flash memory contents. (15115)

Workaround:
1. Control the power supplies so that:
   a. $V_{CORE} \geq V_{CORE(MIN)}$ before $V_{DD} > V_{DD\_RST(MIN)}$ on power up
   b. $V_{DD} < V_{DD\_RST(MIN)}$ before $V_{CORE} \leq V_{CORE(MIN)}$ on power down

or

2. Ensure RSTN is asserted active whenever $V_{CORE} \leq V_{CORE(MIN)}$.

Analog Devices

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	8/22	Initial release	—
1	12/23	Removed MAX32671 from title. Removed items #1 and #2. Added new errata 1, 2, and 3.	1, 2