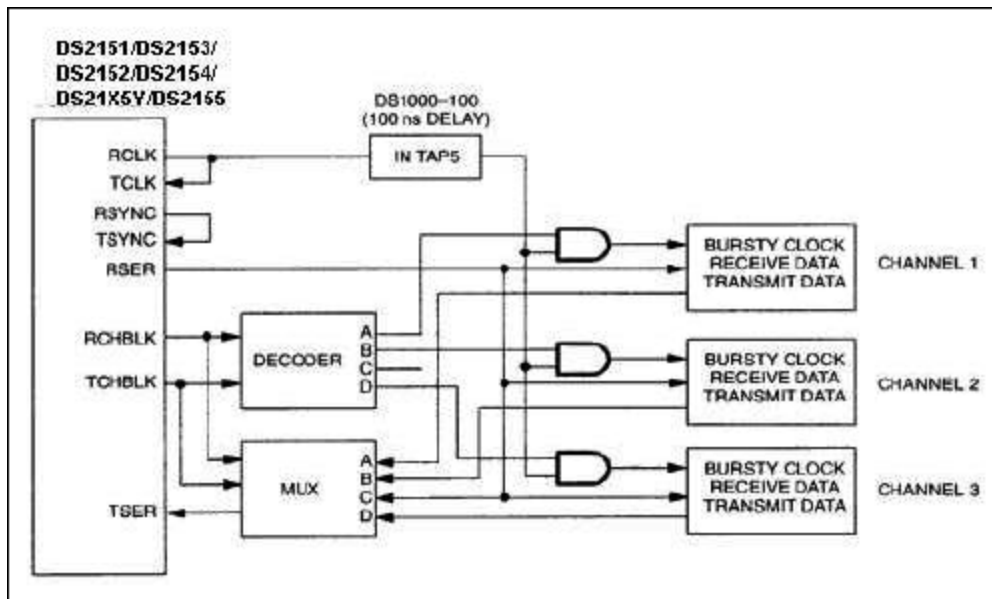Keywords: three channel drop, three channel insert, transceivers, DS2152, DS2154, DS2151, DS2153, DS21X5Y, DS2155

APPLICATION NOTE 307

DS2152, DS2154, DS2151, DS2153, DS21X5Y and DS2155 Three Channel Drop and Insert

Feb 15, 2002

Abstract: This application assumes the dropped channels occupy the same timeslots as the inserted channels. The idle registers in the DS2152, DS2154, DS21X5Y and DS2155 can be used to fill unused (if any) channels. The used channels do not need to be contiguous nor do they need to be only one channel wide. The DS1000-100 delay line is used to adjust the clock to accounts for the delay in generation of the signals that create the bursty clock. Additional channels can be added by allowing the RCHCLK signal to be driven into a counter and decoded; the decoded signal would then be used to provide additional selects for the decoder and mux.

Notes:

1. A "looped-timed" application is shown.
2. This application assumes the dropped channels occupy the same timeslots as the inserted channels.
3. The idle registers in the DS2152, DS2154, DS21X5Y, and DS2155 can be used to fill unused (if any) channels.

4. The used channels do not need to be contiguous nor do they need to be only one channel wide.
5. The DS1000-100 delay line is used to adjust the clock to account for the delay in signal generations that create the bursty clock.
6. Additional channels can be added by allowing the RCHCLK signal to be driven into a counter and decoded; the decoded signal would then be used to provide additional selects for the decoder and mux.

Related Parts		
DS21352	3.3V DS21352 and 5V DS21552 T1 Single Chip Transceivers	
DS21354	3.3V/5V E1 Single Chip Transceivers (SCT)	
DS2152	Enhanced T1 Single Chip Transceiver	
DS2154	Enhanced E1 Single Chip Transceiver	
DS2155	T1/E1/J1 Single-Chip Transceiver	Free Samples
DS21552	3.3V DS21352 and 5V DS21552 T1 Single Chip Transceivers	
DS21554	3.3V/5V E1 Single Chip Transceivers (SCT)	Free Samples

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