



DESIGN NOTES

Electrically Isolating Data Acquisition Systems

Design Note 10

Guy Hoover and William Rempfer

Introduction

In data acquisition systems it is often necessary to electrically isolate the measurement points from the system controller. Reasons for the electrical isolation include the following: to allow floating measurements at high voltages; for safety, to reduce the danger of electrical shock, as might occur in medical applications; and to eliminate ground loops between measurement points and the system controller which can cause errors.

The data transmitted over the isolated lines can be either analog or digital. Analog signals have poor noise immunity and one isolator is required for each signal point. Traditionally, the highly noise immune, digitally encoded signals required many isolated lines for each channel. Now, with the LTC[®]1090 family of serial data acquisition systems, it is possible to transmit eight channels of data with only four isolated lines. Each additional eight channels requires only one additional isolated line.

Both opto isolators and pulse transformers could be used to isolate the signals. However, since opto isolators tend to be smaller and less expensive than pulse transformers, they will be the only type of device considered here.

The circuit to be demonstrated is an eight channel data acquisition system with 500V of isolation that uses the LTC1090 and four opto isolators. With the addition of another opto isolator, the circuit can be battery operated, drawing only 50 μ A while taking a reading once every two seconds.

The number of channels can be increased to 16, 24, 32, etc., with one additional opto isolator used to increase the number of channels in multiples of eight. Up to 24 channels can be powered directly by the LT1021.

Circuit Description

The LT1021 powers the analog circuitry and provides an accurate reference. A 1 Ω resistor isolates the reference from power supply transients.

The 4N28s in Figure 1 are very commonly used opto isolators. They provide only 500V of isolation, however. If more isolation is desired, up to 2500V of isolation can be obtained by using 4N25s with no other circuit modifications.

PNP transistors were chosen to drive the opto isolators to optimize signal fall time and clock rate. D_{OUT} of the LTC1090 is transmitted on the falling edge of SCLK. Data is clocked into the processor on the rising edge of SCLK. It is therefore necessary that the falling edge of SCLK have as little delay as possible through the opto isolator. This insures that D_{OUT} can be output by the LTC1090 in time to be captured by the processor on the rising edge of SCLK. NPNs could be used at slower data rates or if burning more current is not objectionable.

The current limiting resistors in the collectors of the opto drivers are chosen with the Current Transfer Ratio (CTR) of the opto isolator in mind. The output transistor of the opto isolator must have enough base current to drive the desired load. The base resistor on the opto output transistor is there to decrease the turn off time of the opto isolator.

The code written for the Motorola 68HC05 processor is available from Linear Technology. The code powers up the circuit, allows 6.0ms for the 10 μ F cap to charge to its final value, reads 9 channels in 12ms, and then powers down until the next set of readings is required. Nine channel readings are required because, when the

LT, LT, LTC, LTM, Linear Technology and the Linear logo are registered trademarks of Linear Technology Corporation. All other trademarks are the property of their respective owners.

