



sensor nonlinearity to within  $\pm 0.05^{\circ}\text{C}$ . A1B, providing additional scaled gain, furnishes the circuit output.

To calibrate this circuit, substitute a precision decade box (e.g., General Radio 1432k) for  $R_p$ . Set the box to the  $0^{\circ}\text{C}$  value ( $100.00\Omega$ ) and adjust the zero trim for a  $0.00\text{V}$  output. Next, set the decade box for a  $140^{\circ}\text{C}$  output ( $154.26\Omega$ ) and adjust the gain trim for a  $3.500\text{V}$  output reading. Finally, set the box to  $249.0\Omega$  ( $400.00^{\circ}\text{C}$ ) and trim the linearity adjustment for a  $10.000\text{V}$  output. Repeat this sequence until all three points are fixed. Total error over the entire range will be within  $\pm 0.05^{\circ}\text{C}$ . The resistance values given are for a nominal  $100.00\Omega$  ( $0^{\circ}\text{C}$ ) sensor. Sensors deviating from this nominal value can be used by factoring in the deviation from  $100.00\Omega$ . This deviation, which is manufacturer specified for each individual sensor, is an offset term due to winding tolerances during fabrication of the RTD. The gain slope of the platinum is primarily fixed by the purity of the material and has a very small error term.

The previous example relies on analog techniques to achieve a precise, linear output from the platinum RTD bridge. Figure 2 uses digital corrections to obtain similar results. A processor is used to correct residual RTD nonlinearities. The bridges inherent nonlinear output is also accommodated by the processor.

The LT1027 drives the bridge with  $5\text{V}$ . The bridge differential output is extracted by instrumentation amplifier A1. A1's output, via gain scaling stage A2, is fed to the LTC1290 12-bit A/D. The LTC1290's raw output codes reflect the bridges nonlinear output versus temperature. The processor corrects the A/D output and presents linearized, calibrated data out. RTD and resistor tolerances mandate zero and full-scale trims, but no linearity correction is necessary. A2's analog output is available for feedback control applications. The complete software code for the 68HC05 processor, developed by Guy M. Hoover, appears in Application Note 43.

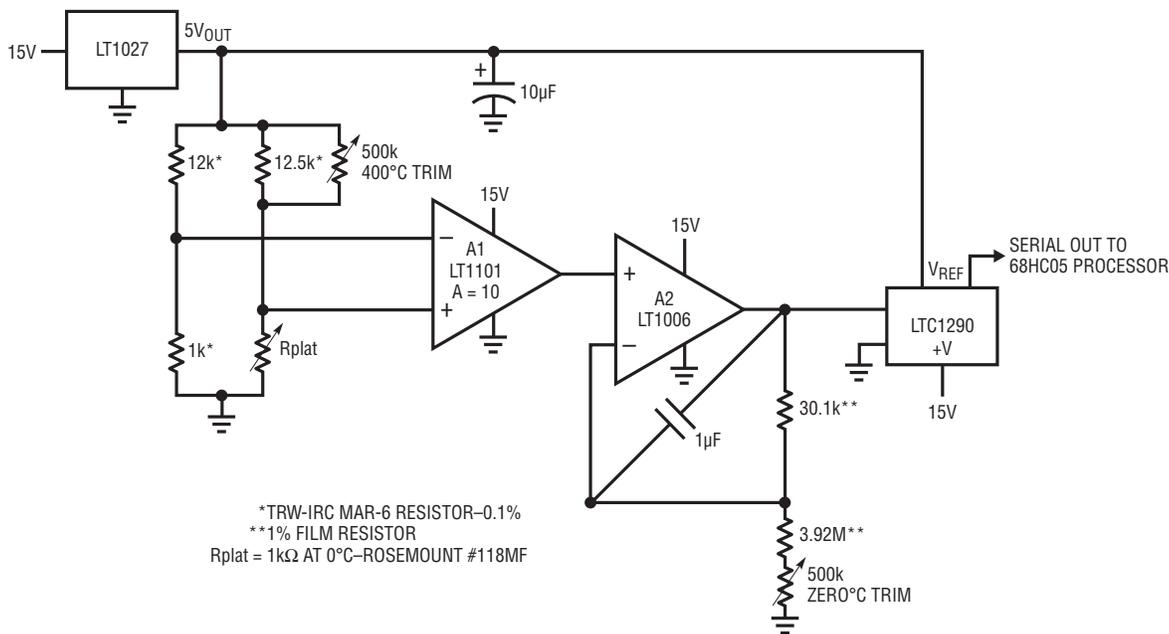


Figure 2. Digitally Linearized Platinum RTD Signal Conditioner

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