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REVISIONS

REV	DESCRIPTION	DATE	APPROVED

SDP-K1 SCHEMATIC

PAGE 2 - USB

PAGE 3 - MCU

PAGE 4 - MCU MISCELLANEOUS SIGNALS

PAGE 5 - PROGRAMMING PROCESSOR

PAGE 6 - POWER ON RESET

PAGE 7 - POWER CIRCUITRY

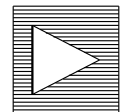
PAGE 8 - ARDUINO CONNECTOR

PAGE 9 - SDP CONNECTOR

PAGE 10 - SDRAM LEVEL TRANSLATORS

PAGE 11 - SDRAM

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER	DATE	SCHEMATIC		 ANALOG DEVICES
HARDWARE SERVICES		HW TYPE : Customer Evaluation		
HARDWARE SYSTEMS		Product(s): N/A		
TEST ENGINEER		: N/A		
COMPONENT ENGINEER		PACKAGE : N/A-lead N/A N/A-family		
TEST PROCESS		: Pitch-pitch StyleVendor Style		
HARDWARE RELEASE		<User Define>		
DESIGNER		<User Define>		
PTD ENGINEER		<User Define>		
CHECKER		<User Define>		

MASTER PROJECT TEMPLATE	TESTER TEMPLATE	DRAWING NO.		REV.
TBD	no_template	02_049259		B
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES				
TOLERANCES				
DECIMALS	FRACTIONS	ANGLES	SIZE	SCALE
X.XX +-0.010	+1/32	+2	D	1:1
X.XXX +-0.005			CODE ID NO.	
			CodeID	
		SHEET 1	OF 11	

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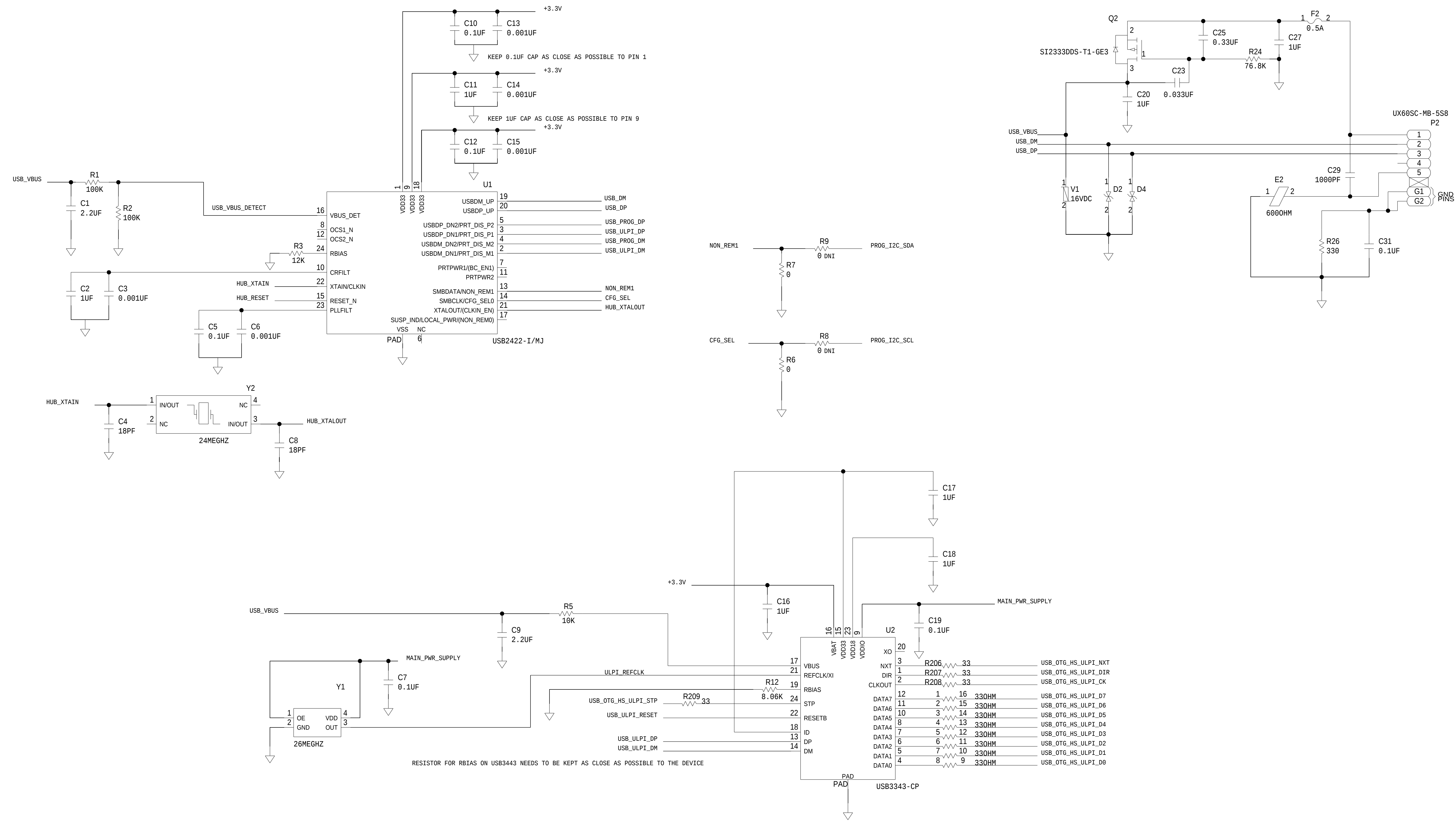
3

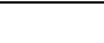
2

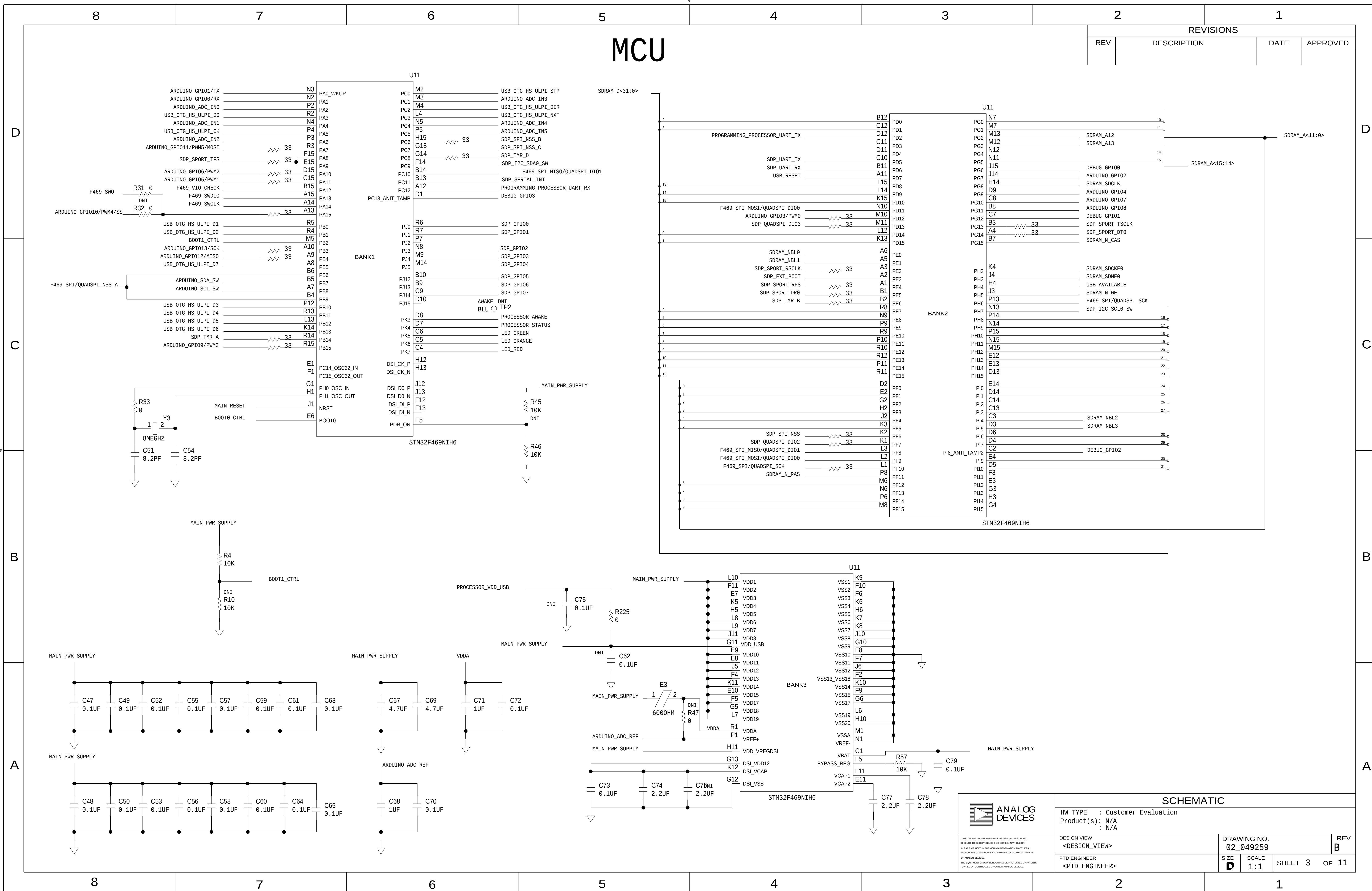
1

USB

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

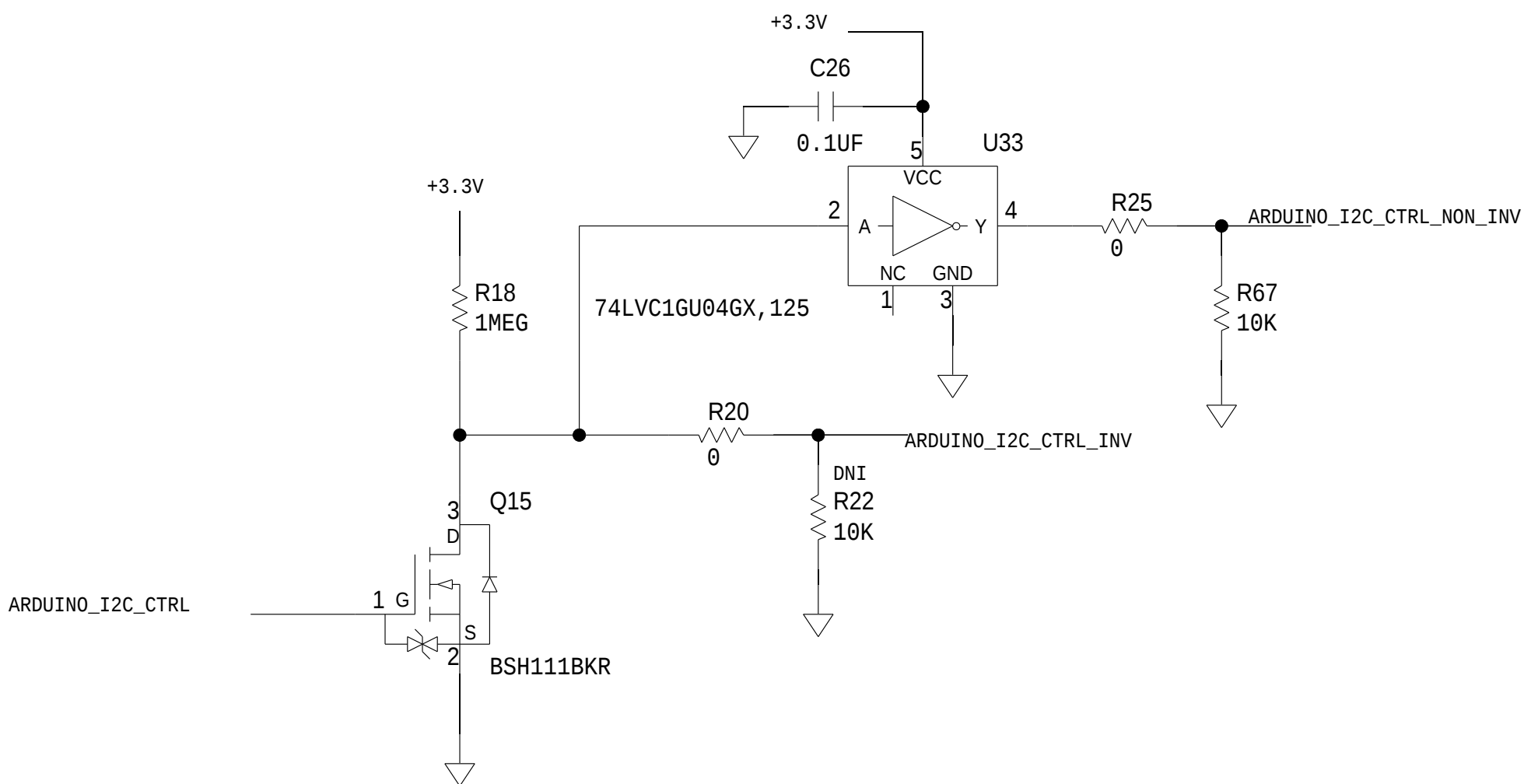
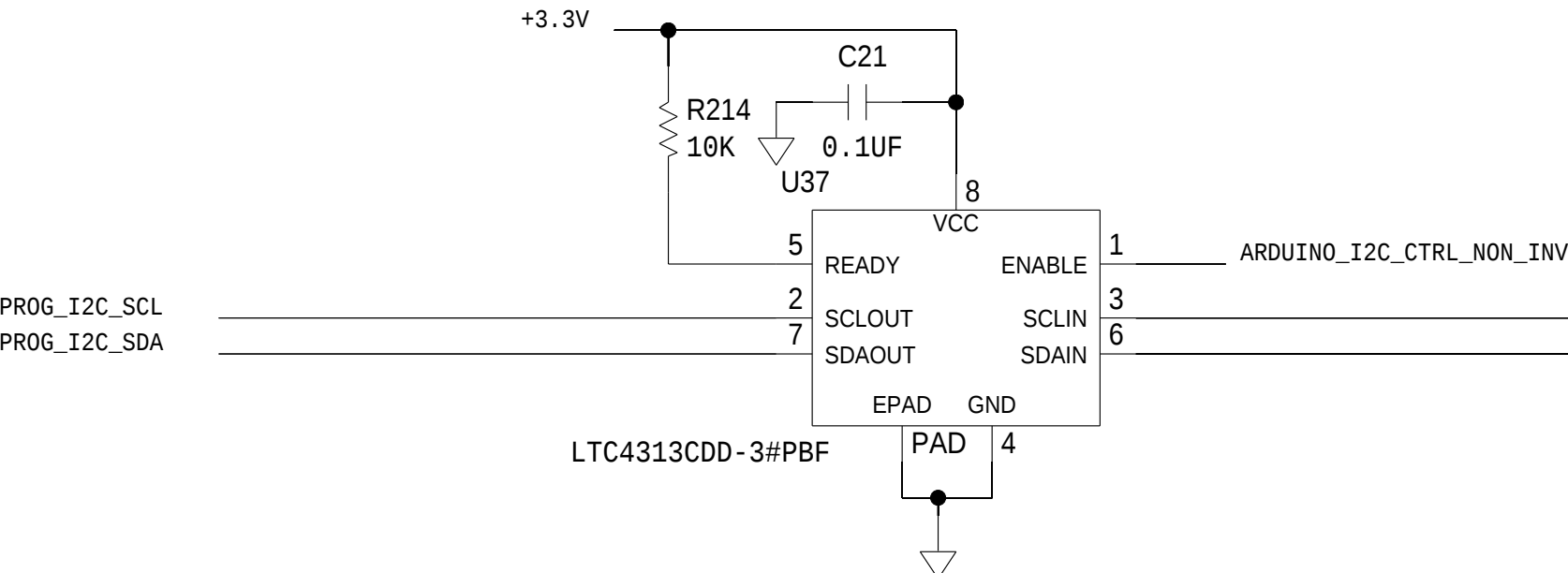
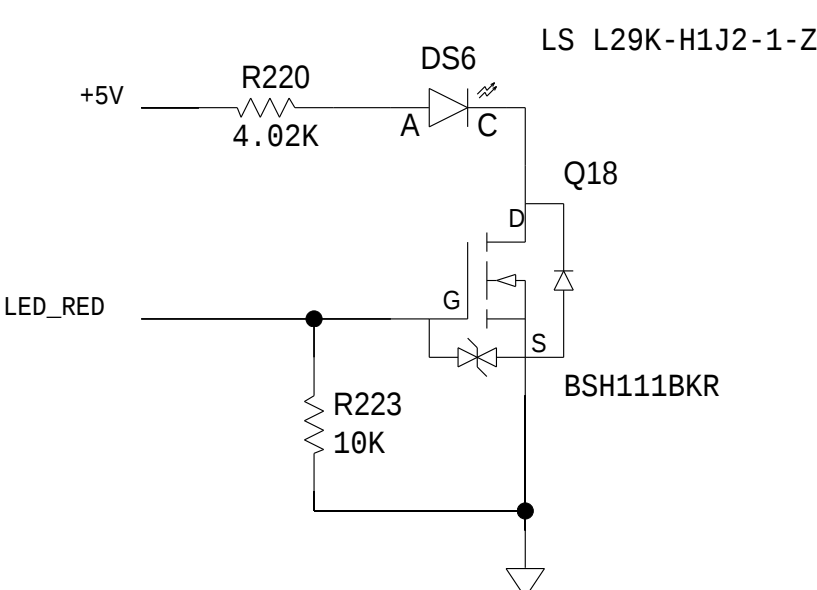
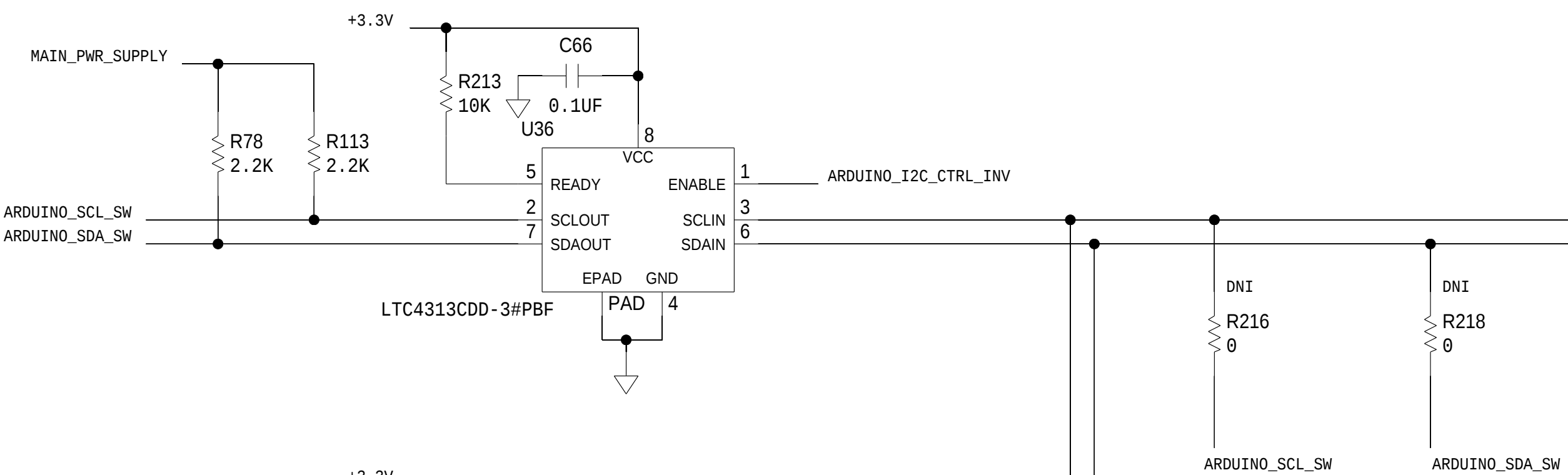
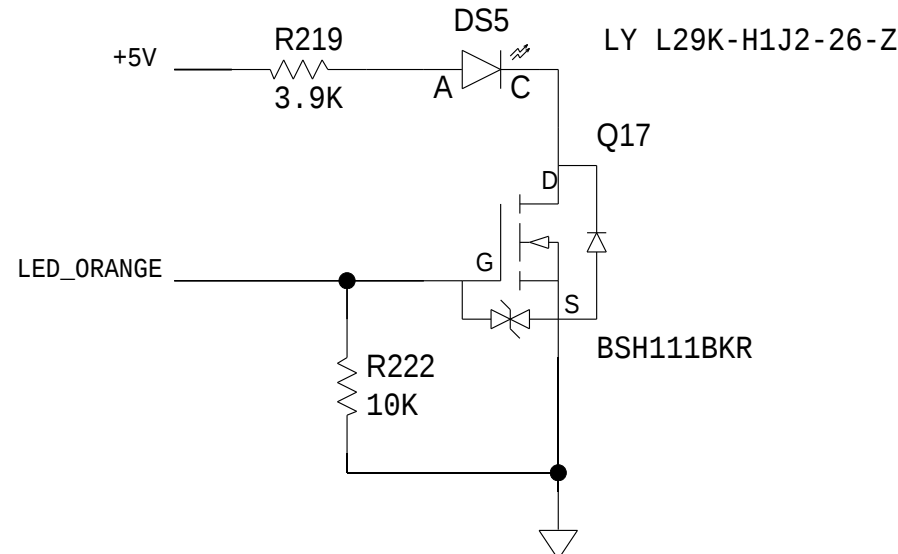
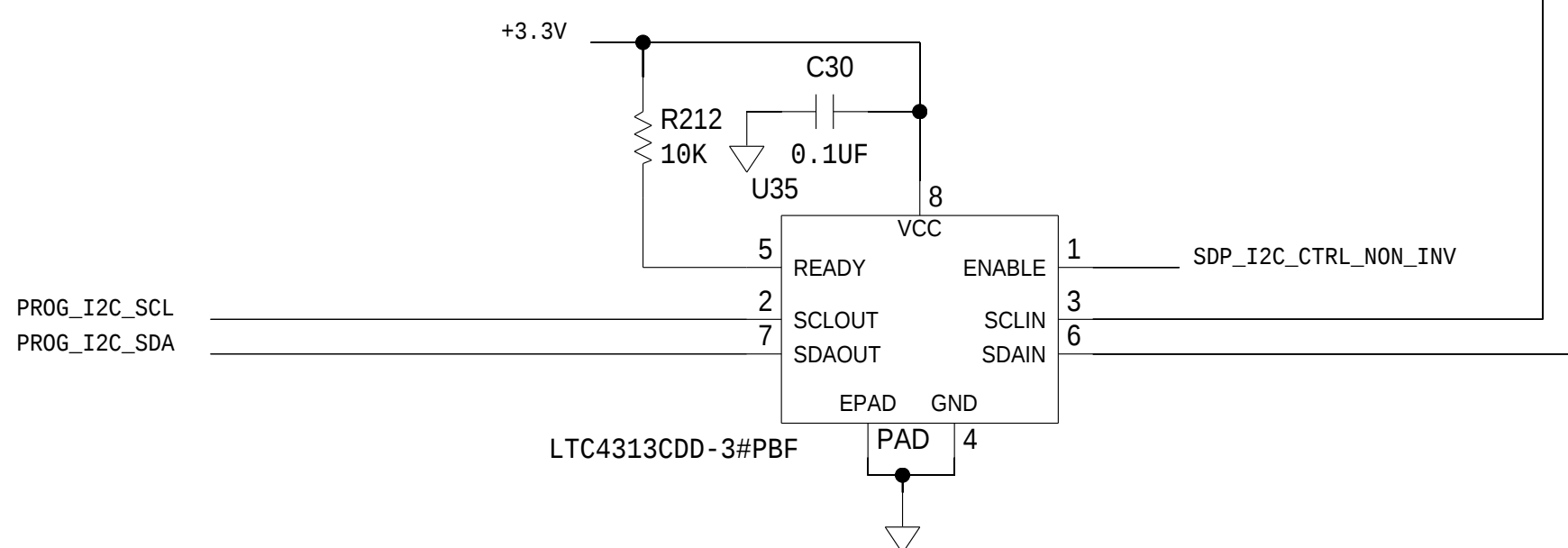
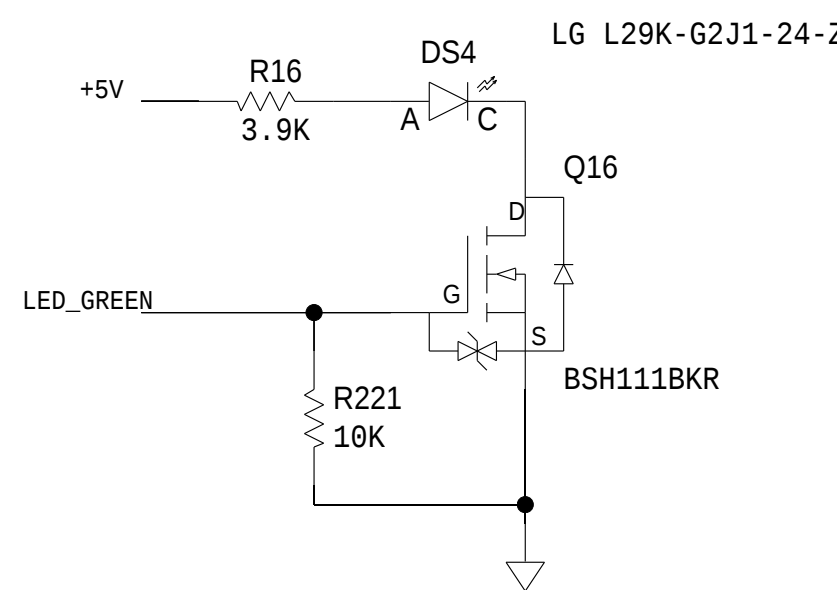
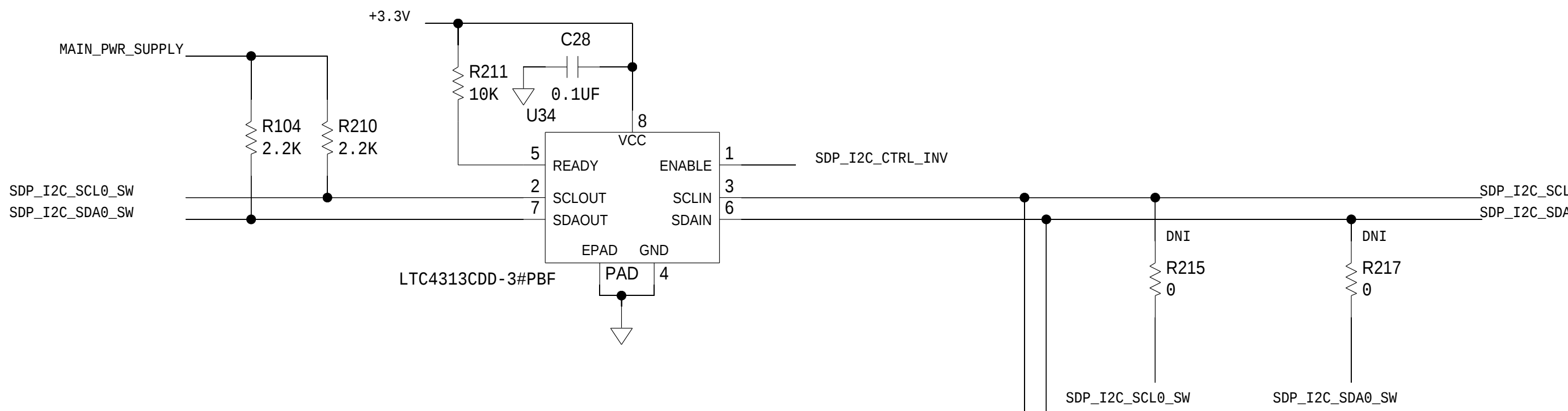
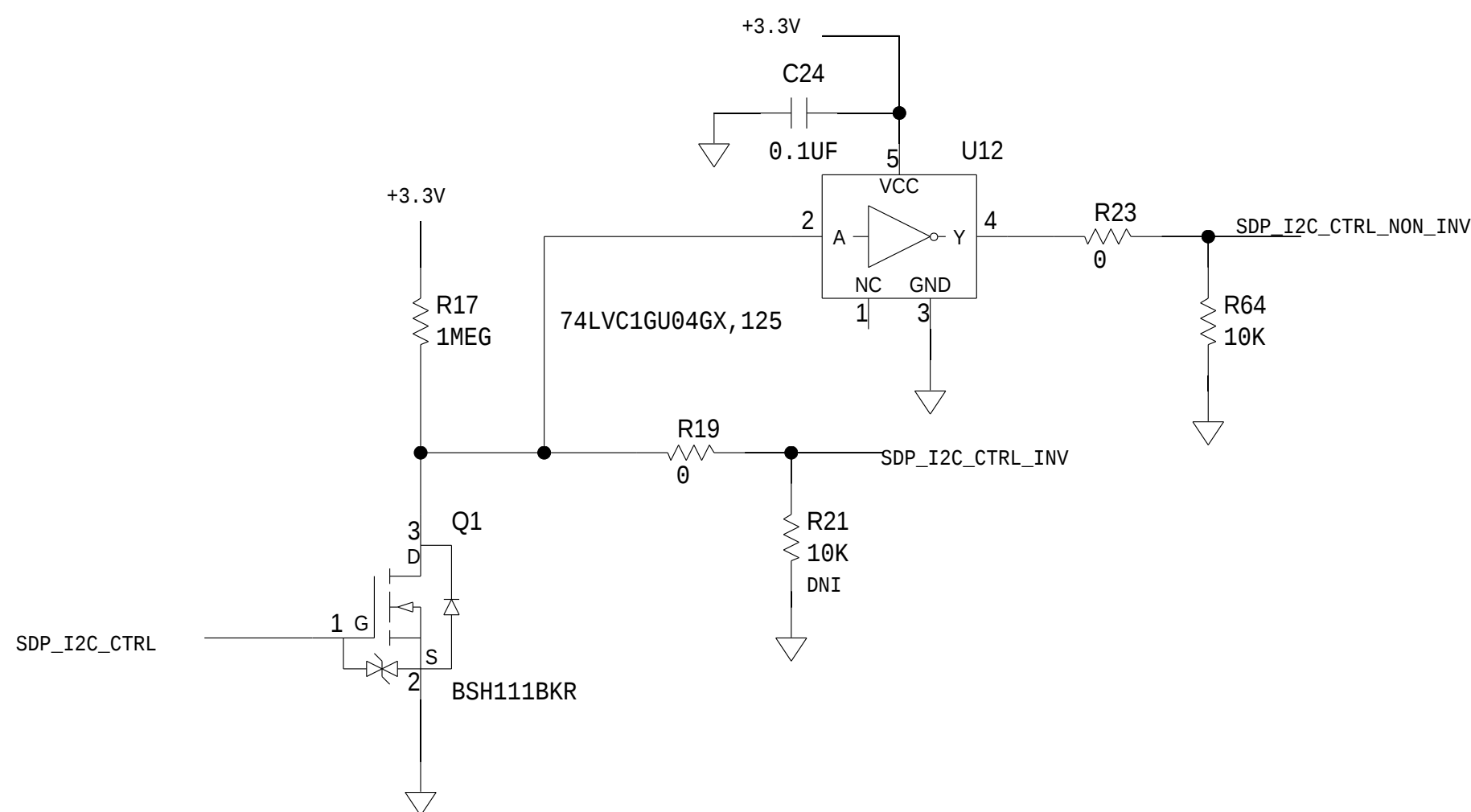
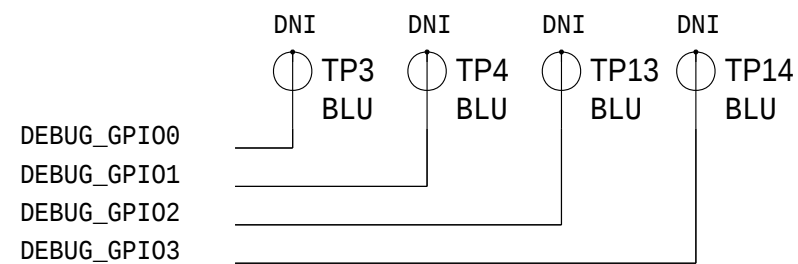
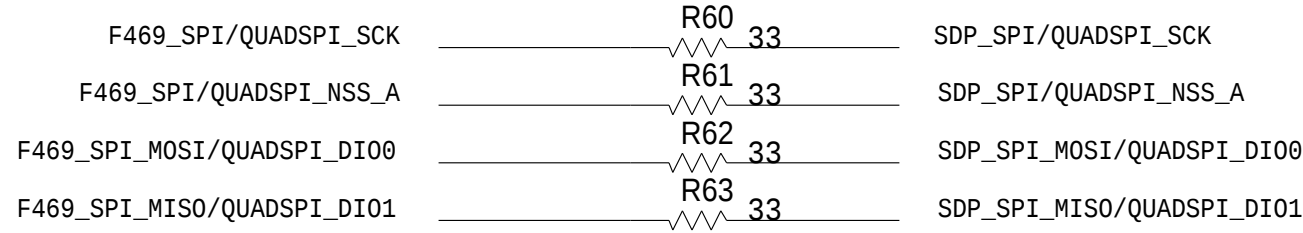
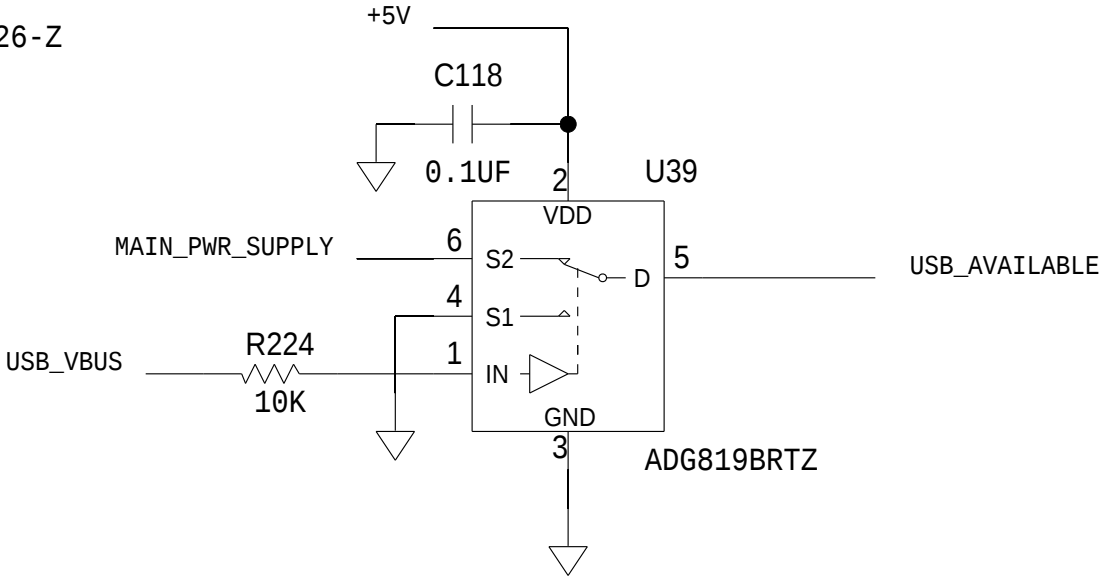
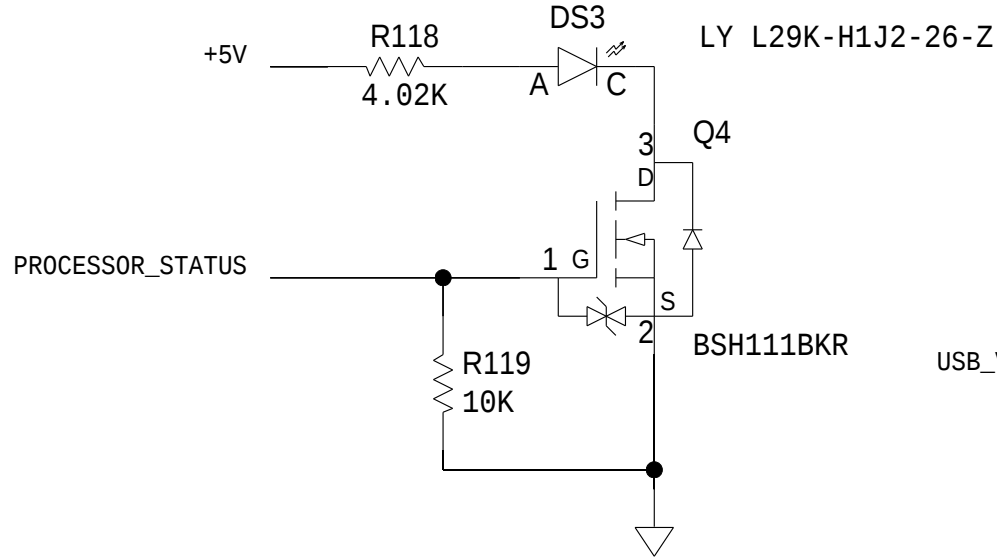
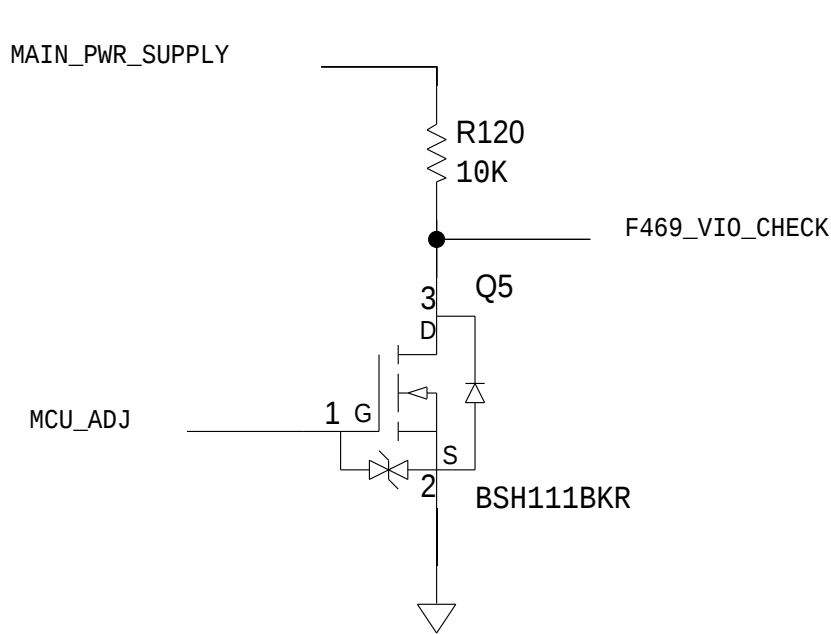


 ANA LOGO DEVICES	SCHEMATIC				
	HW TYPE : Customer Evaluation Product(s) : N/A : N/A				
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	PDT ENGINEER <PDT_ENGINEER>	SIZE D	SCALE 1:1	SHEET 2	OF 11



MCU MISCELLANEOUS SIGNALS

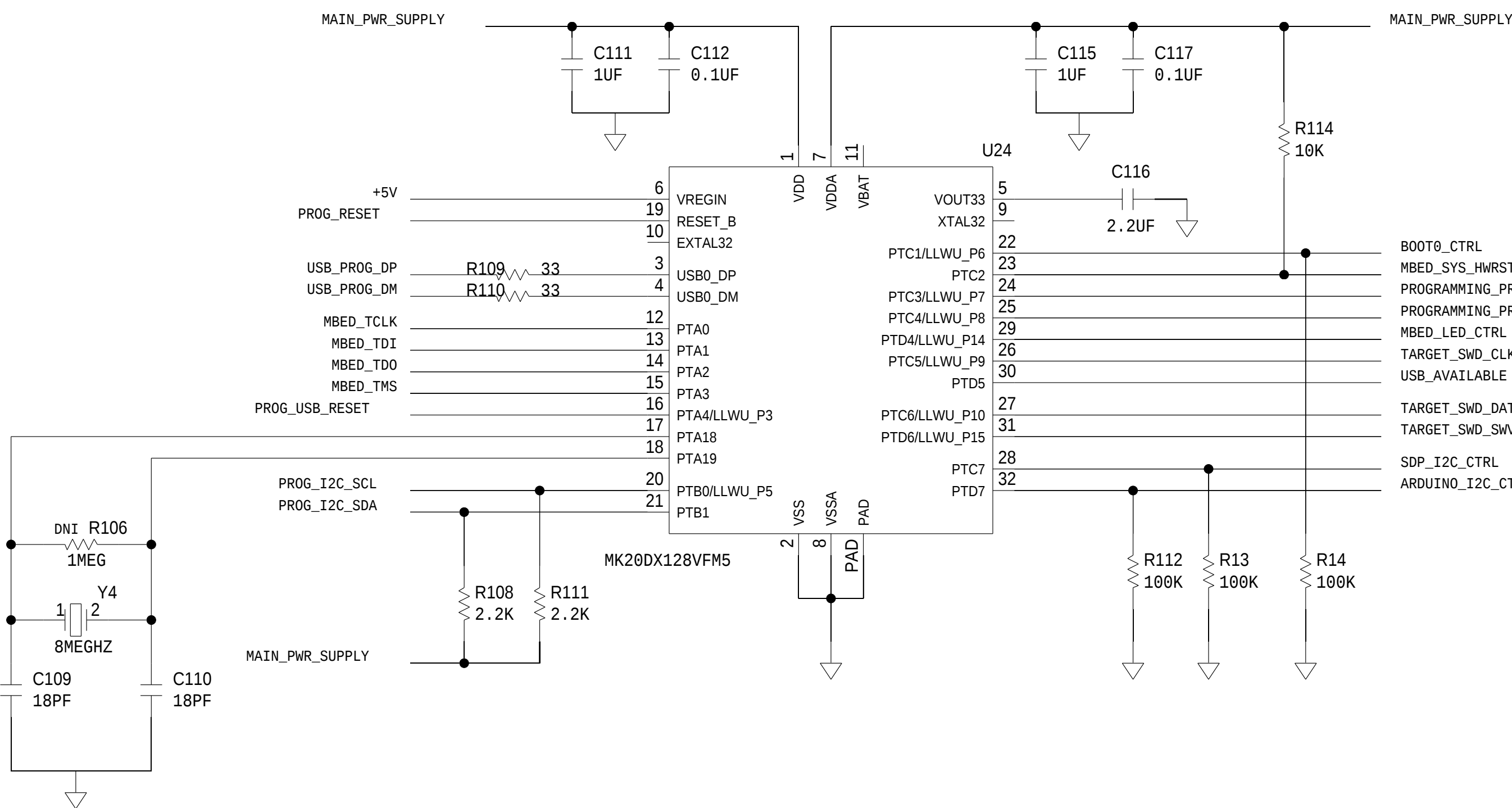
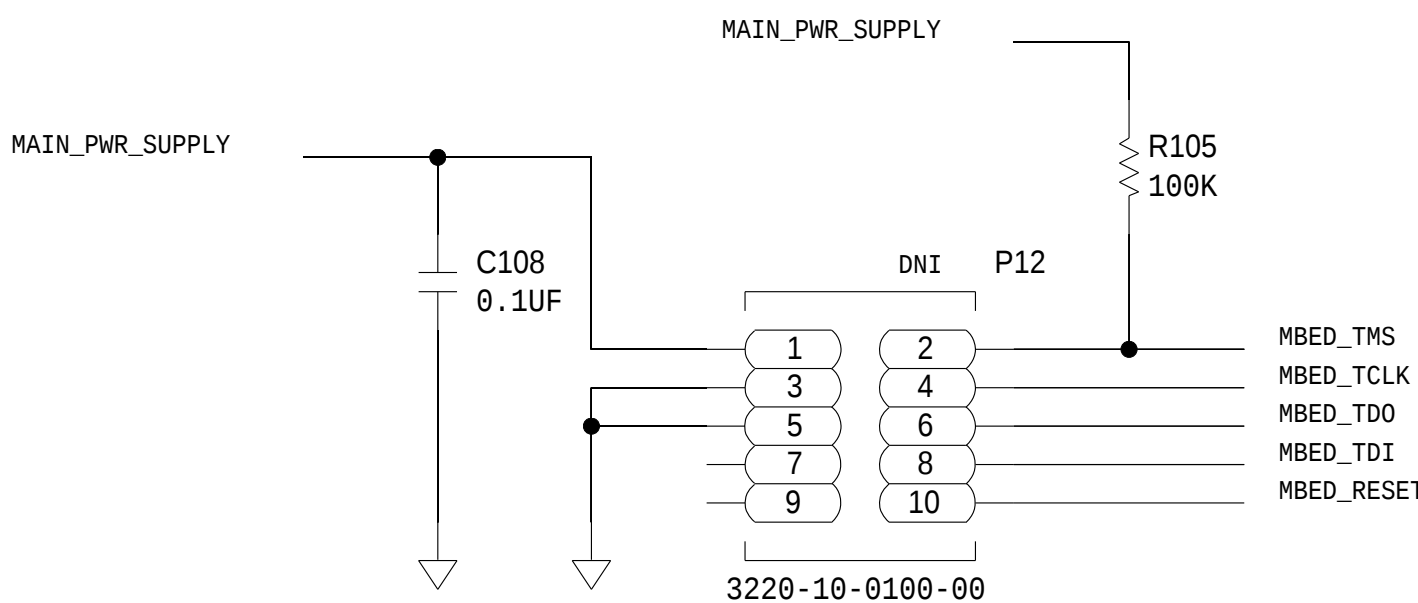
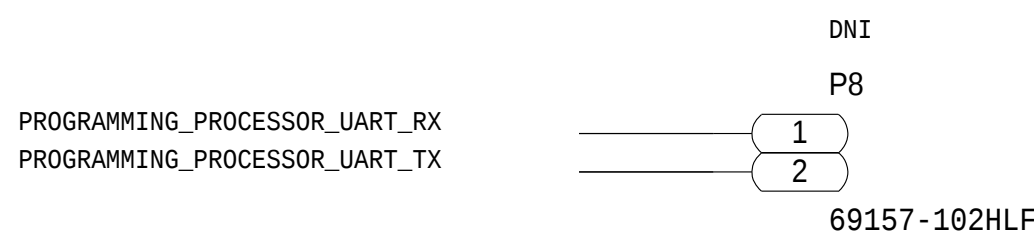
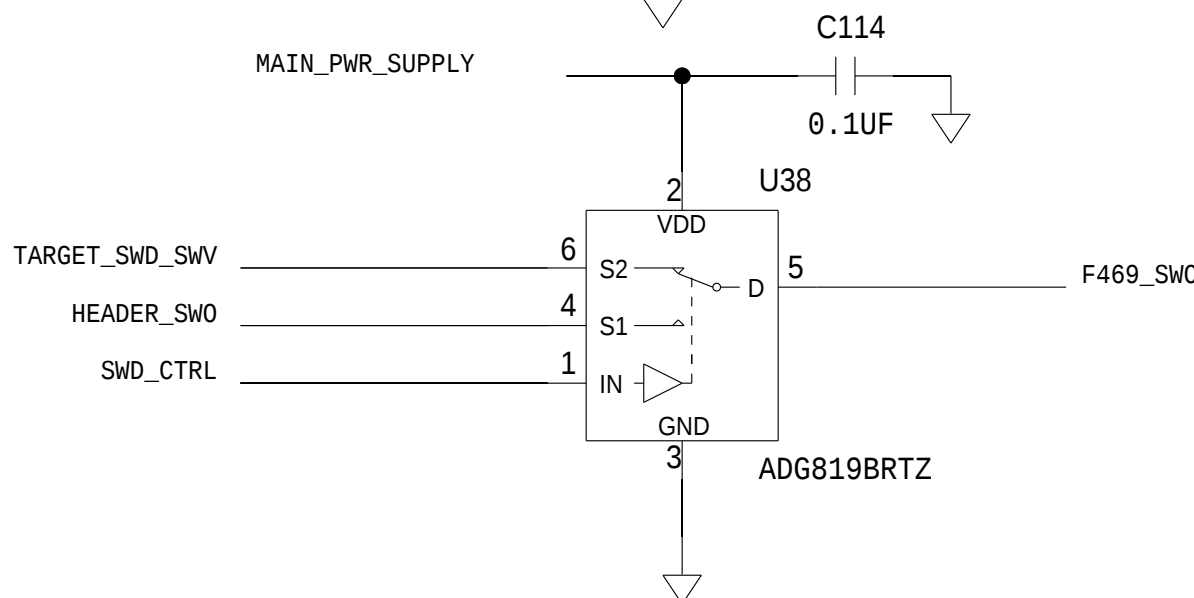
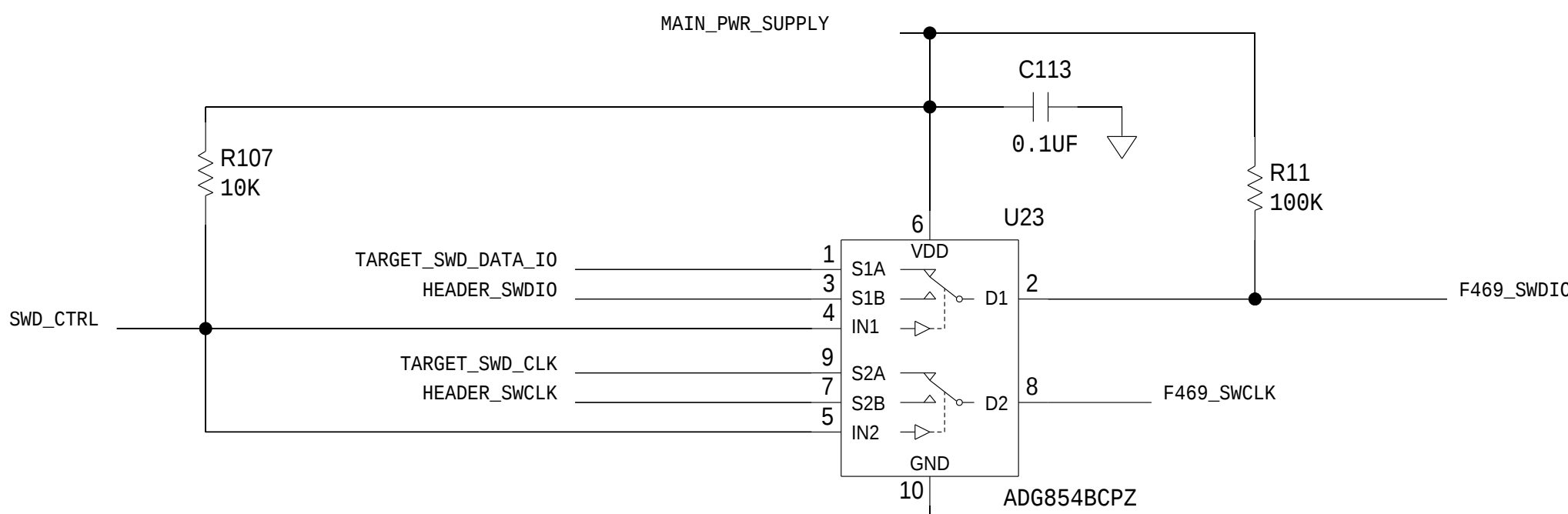
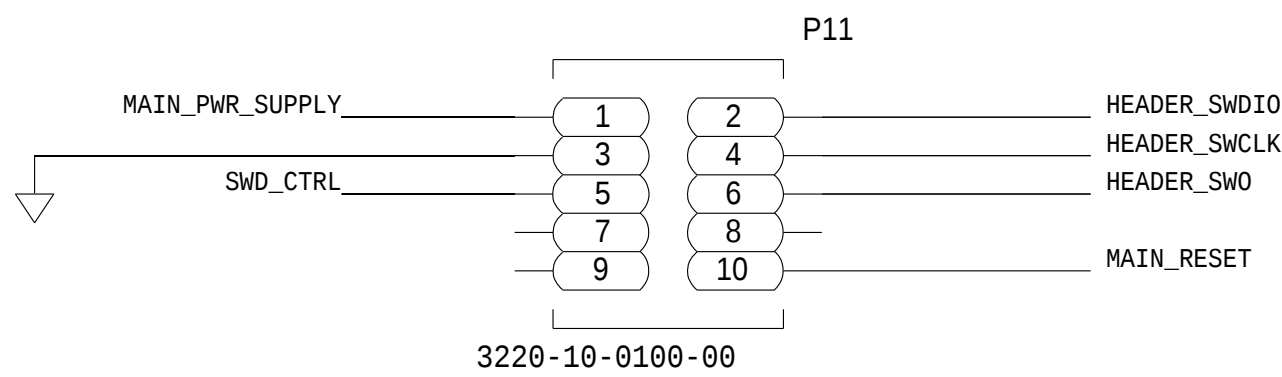
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

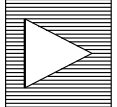
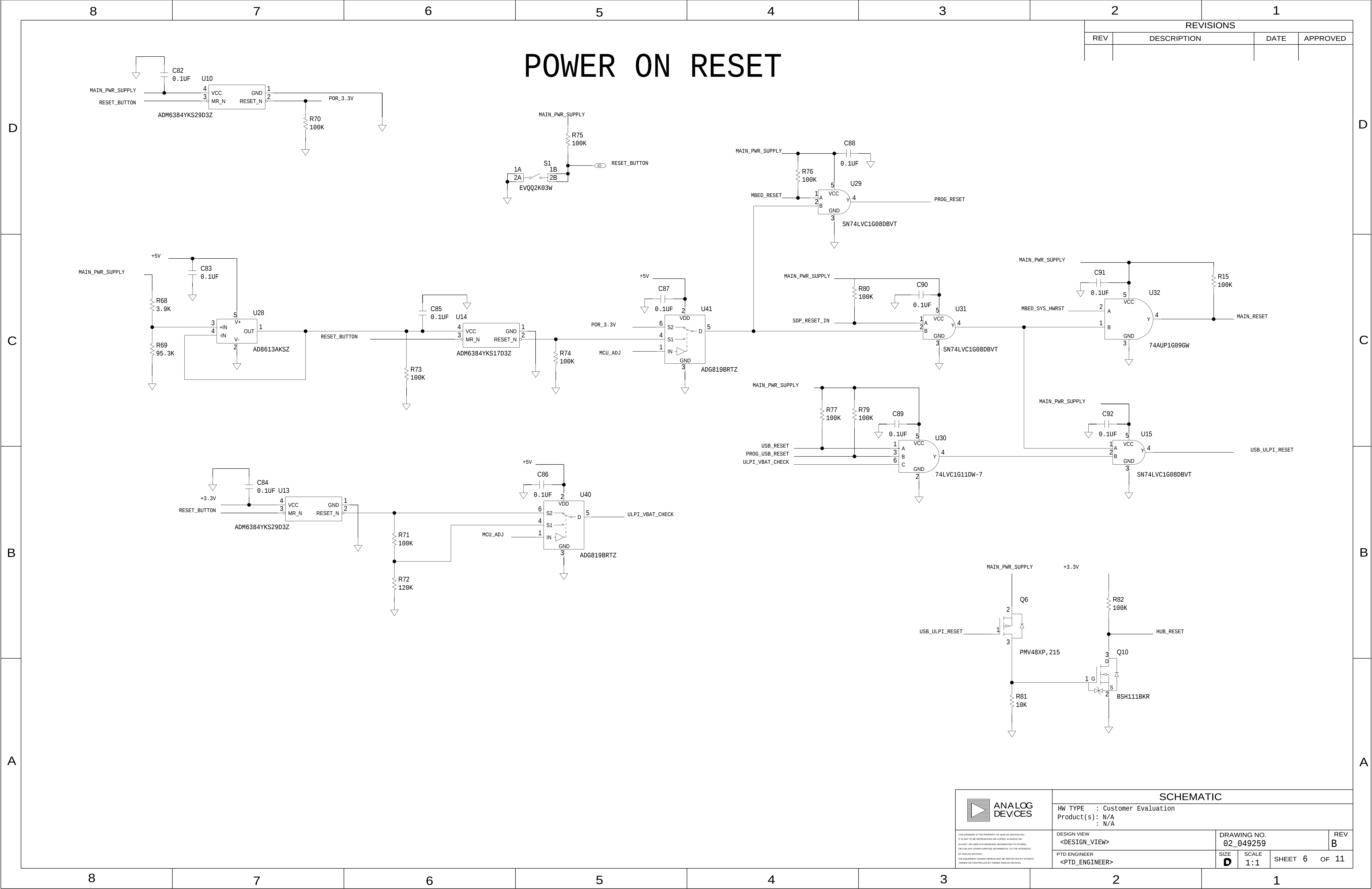


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049259	REV B	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 4 OF 11

PROGRAMMING PROCESSOR

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED





ANALOG
DEVICES

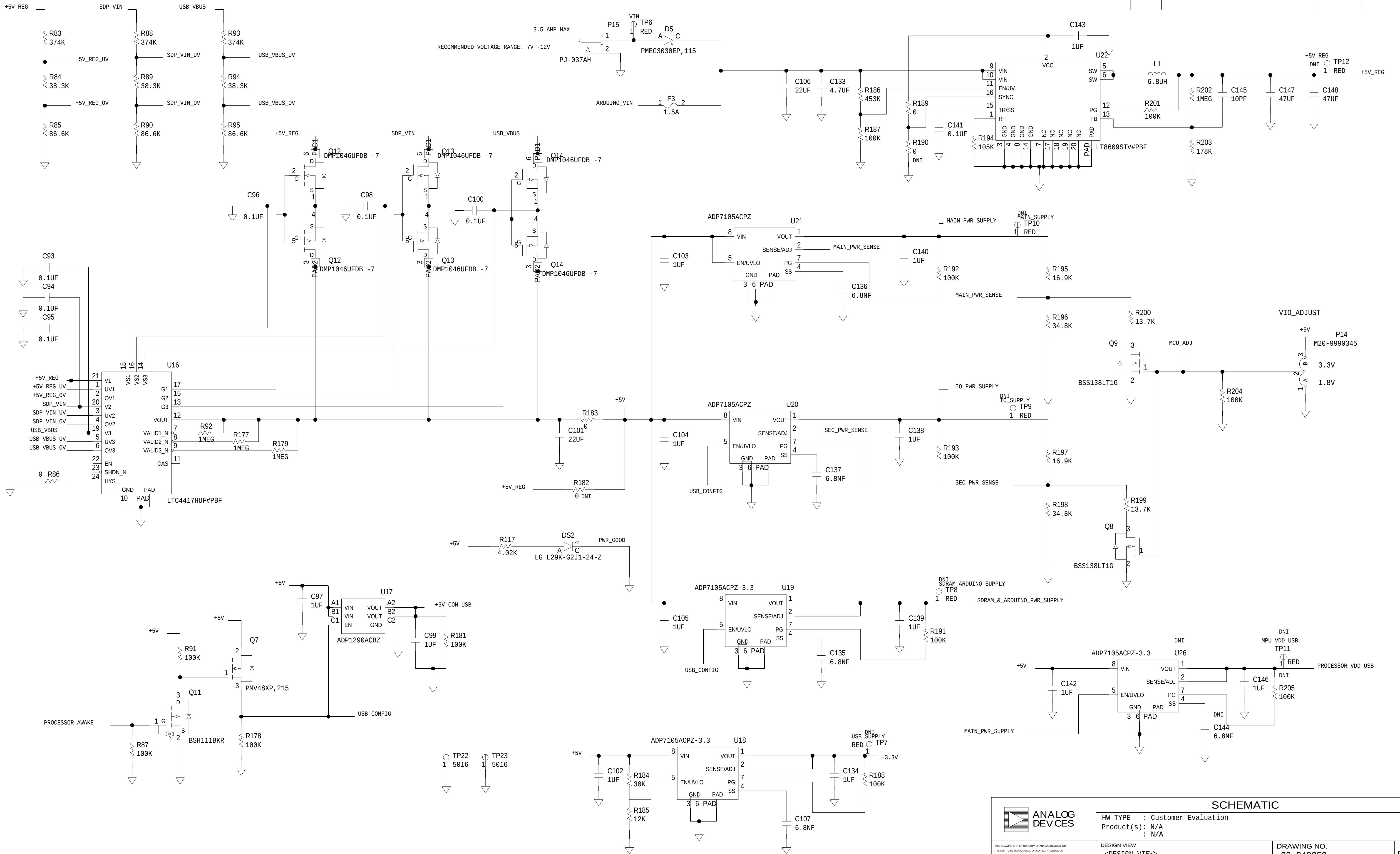
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SCHEMATIC

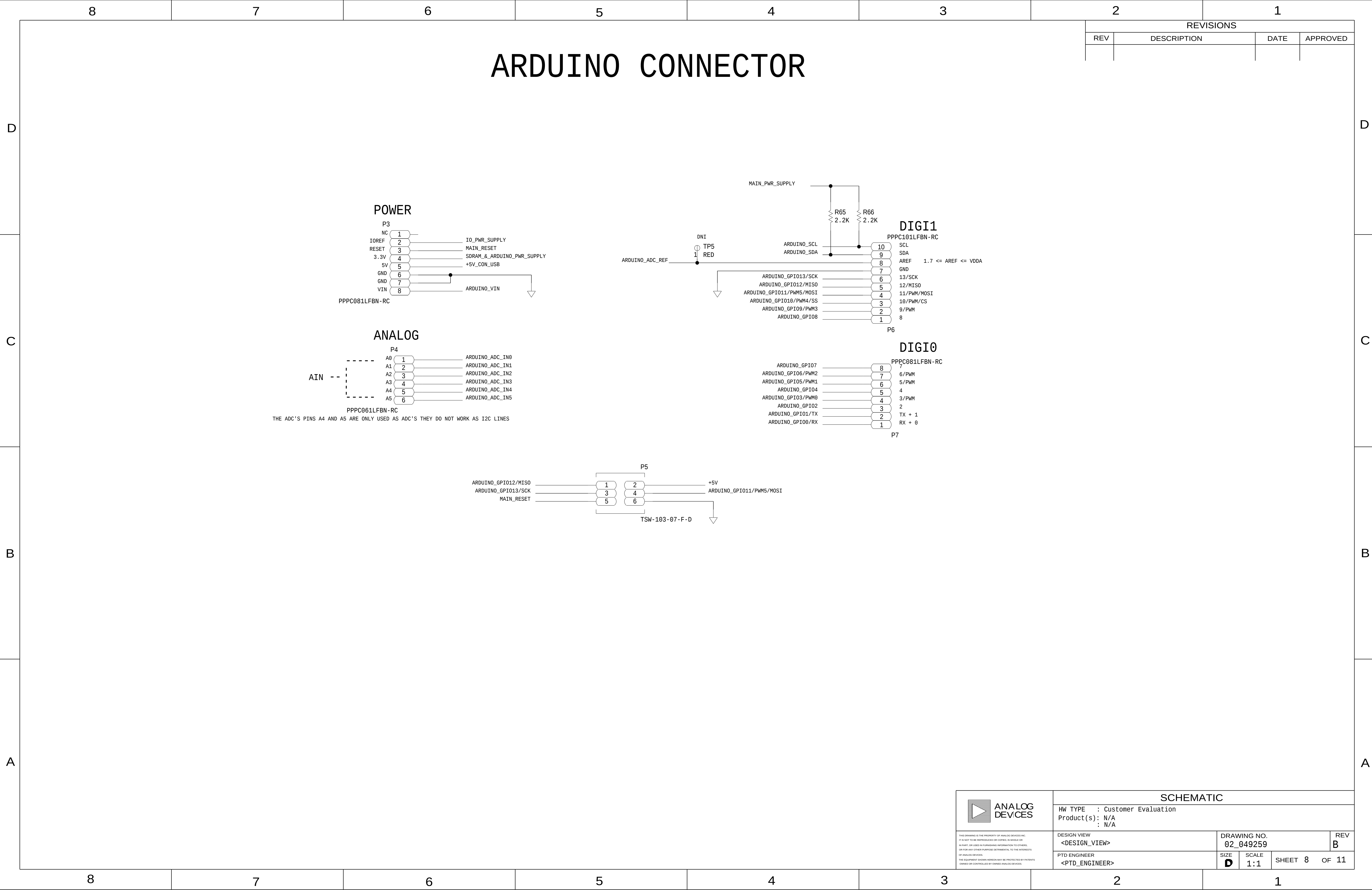
HW TYPE : Customer Evaluation
Product(s): N/A
: N/A

POWER CIRCUITRY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



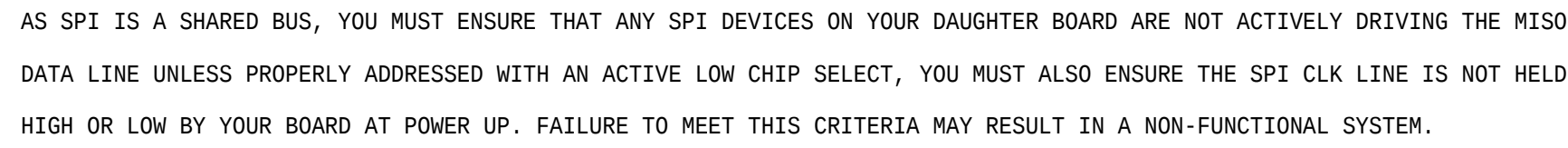
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : N/A : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049259	REV B	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 7 OF 11



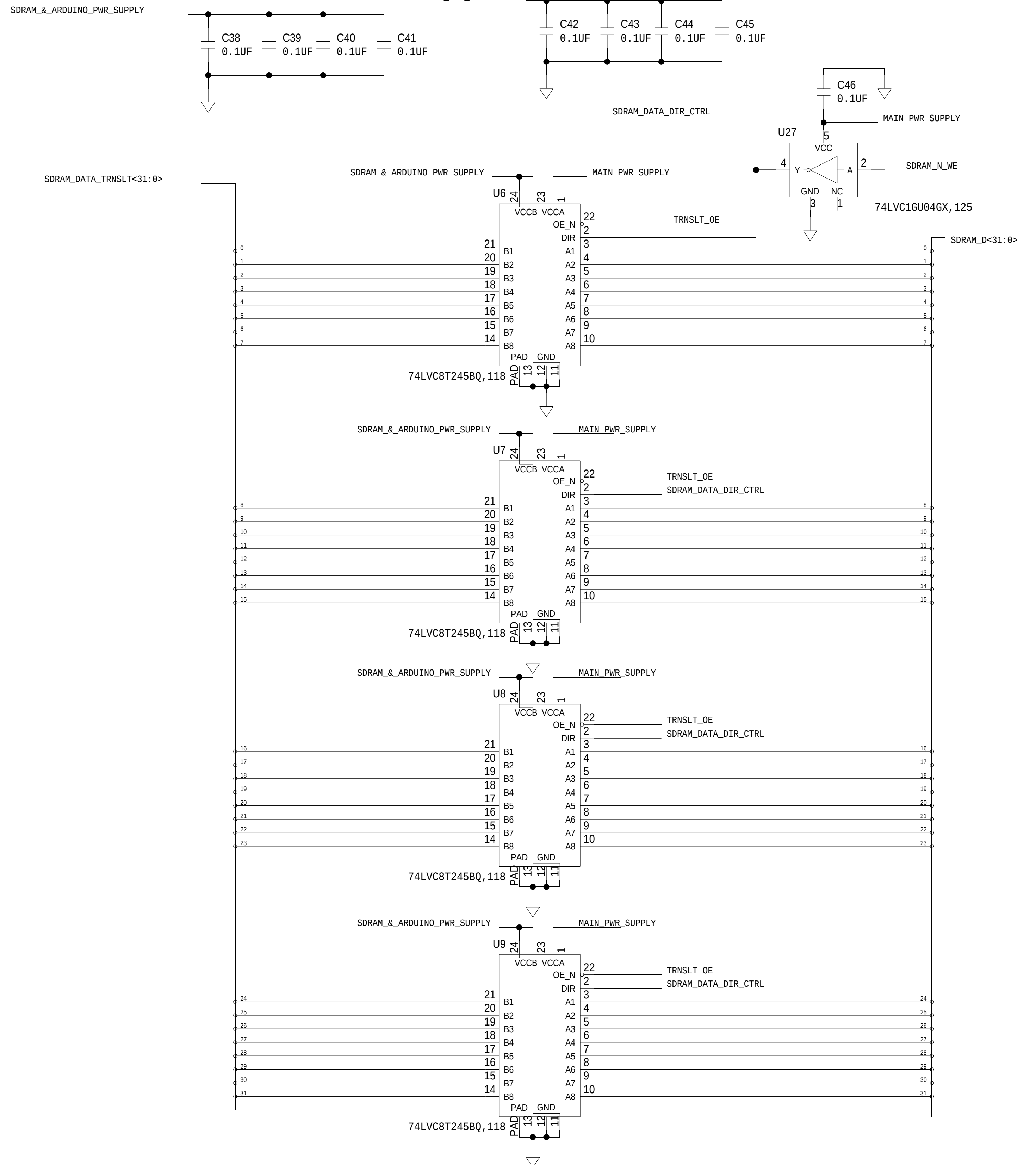
87654321

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

WHEN DESIGNING A DAUGHTER BOARD THE ID EEPROM (24LC32) MUST BE ON I2C BUS 0.
CARE MUST BE TAKEN TO ENSURE DAUGHTER BOARD I2C EEPROM ADDRESS
IS NOT 0X50 OR 0X51. THIS IS TO ENSURE THE I2C ADDRESS RANGE DOES
NOT OVERLAP THAT OF THE E13 BREAK-OUT BOARD.



REVISIONS			
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```
HW TYPE      : Customer Evaluation
Product(s)   : N/A
              : N/A
```

DESIGN VIEW	<DESIGN_VIEW>
PTD ENGINEER	<PTD_ENGINEER>

DRAWING NO.
02 049259

SIZE	SCALE	
D	1:1	

REV
3

SHEET 10 OF 11

