

RELIABILITY REPORT  
FOR  
MAX11122ATI+, MAX11122ATI+T

June 25, 2020

**MAXIM INTEGRATED**

160 RIO ROBLES  
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## Conclusion

The MAX11122 successfully meets the quality and reliability standards required of all Maxim Integrated products. In addition, Maxim Integrated's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim Integrated's quality and reliability standards.

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## I. Device Description

### A. General

The MAX11120-MAX11128 are 12-/10-/8-bit with external reference and industry-leading 1.5MHz, full linear bandwidth, high speed, low-power, serial output successive approximation register (SAR) analog-to-digital converters (ADCs). The MAX11120-MAX11128 includes both internal and external clock modes. These devices feature scan mode in both internal and external clock modes.

The internal clock mode features internal averaging to increase SNR. The external clock mode features the SampleSet(tm) technology, a user-programmable analog input channel sequencer. The SampleSet approach provides greater sequencing flexibility for multichannel applications while alleviating significant microcontroller or DSP (controlling unit) communication overhead. The internal clock mode features an integrated FIFO allowing data to be sampled at high speeds and then held for readout at any time or at a lower clock rate. Internal averaging is also supported in this mode improving SNR for noisy input signals. The devices feature analog input channels that can be configured to be single-ended inputs, fully differential pairs, or pseudo-differential inputs with respect to one common input. The MAX11120-MAX11128 operates from a 2.35V to 3.6V supply and consumes only 5.4mW at 1Msps. The

MAX11120-MAX11128 includes AutoShutdown(tm), fast wake-up, and a high-speed 3-wire serial interface. The devices feature full power-down mode for optimal power management.

The 16MHz, 3-wire serial interface directly connects to SPI, QSPI&#8482, and MICROWIRE&#8482 devices without external logic.

Excellent dynamic performance, low voltage, low power, ease of use, and small package size make these converters ideal for portable battery-powered data-acquisition applications, and for other applications that demand low power consumption and small space.

## II. Manufacturing Information

A. Description/Function:	1Msps, Low-Power, Serial 12-/10-/8-Bit, 4-/8-/16-Channel AD
B. Process:	TS18
C. Device Count:	158486
D. Fabrication Location:	Taiwan
E. Assembly Location:	China, Thailand, Taiwan
F. Date of Initial Production:	June 2012

## III. Packaging Information

A. Package Type:	28L TQFN
B. Lead Frame:	Copper
C. Lead Finish:	Matte Tin
D. Die Attach:	AB8200T/EN4900G
E. Bondwire:	1 mil Au
F. Mold Material:	G770HCD, G770HJ
G. Flammability Rating:	UL-94 (V-0 Rating)
H. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
I. Single Layer Theta Ja:	47 °C/W
J. Single Layer Theta Jc:	2 °C/W
K. Multi Layer Theta Ja:	29 °C/W
L. Multi Layer Theta Jc:	2 °C/W

## IV. Die Information

A. Dimensions:	65 x 87 mils
B. Passivation:	SiN / SiO <sub>2</sub>

## V. Quality Assurance Information

<b>A. Quality Assurance Contacts:</b>	Ryan Wall (Manager, Reliability) Michael Cairnes (Executive Director, Reliability) Bryan Preeshl (SVP of QA)
<b>B. Outgoing Inspection Level:</b>	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% for all Visual Defects.
<b>C. Observed Outgoing Defect Rate:</b>	< 50 ppm
<b>D. Sampling Plan:</b>	Mil-Std-105D

## VI. Reliability Evaluation

### A. Accelerated Life Test

The results of the 135C biased (static) life test are shown in Table 1. Using these results, the Failure Rate  $\lambda$  is calculated as follows:

$$\lambda = \frac{1}{MTTF} = \frac{1.83}{192 \times 4340 \times 77 \times 2} \text{ (Chi square value for MTTF upper limit)}$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 14.3 \times 10^{-9}$$

$$\lambda = 14.3 \text{ FITs (60\% confidence level @25°C)}$$

Maxim Integrated performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at <https://www.maximintegrated.com/en/support/qa-reliability/reliability/reliability-monitor-program.html>.

TS18 cumulative process data:

$$\lambda = 0.03 \text{ FITs (60\% confidence level @25°C)}$$

$$\lambda = 0.35 \text{ FITs (60\% confidence level @55°C)}$$

### B. ESD and Latch-Up Testing

The MAX11122 has been found to have all pins able to withstand an HBM transient pulse of  $\pm 2500$  V per JEDEC / ESDA JS-001. Latch-Up testing has shown that this device withstands  $\pm 100$  mA current injection and supply overvoltage per JEDEC JESD78.

**Table 1**  
Reliability Evaluation Test Results  
**MAX11125ATI+ (Note 1)**

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (Note 2)	Ta = 135°C Biased Time = 192 hrs.	DC parameters & functionality	77	0	

Note 1: MAX11125ATI+ and MAX11122ATI+ are same silicon.

Note 2: Life Test Data may represent plastic DIP qualification lots.