

RELIABILITY REPORT FOR MAX151BCNG+

PLASTIC ENCAPSULATED DEVICES

March 24, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

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MAX151BCNG+

Conclusion

The MAX151BCNG+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

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The MAX151 is a high-speed, easy-to-use, microprocessor (μ P) compatible 10-bit Analog-to-Digital Converter (ADC) with Track-and-Hold (T/H). Half-flash techniques allow a typical conversin time of 1.9 μ s with a Total Unadjusted Error (TUE) of ±1 (LSB (max)). The converter has a 0V to +5V analog input voltage range and uses ± supplies.

The MAX151 internally tracks and holds the analog input signal, eliminating the need for an external T/H when digitizing high-speed AC input signals. the MAX151 also contains an internal 4V reference, making the part a complete, low-cost ADC. Power consumption is typically 275mW.

The Max151 interfaces directly to a μ P by appearing as a memory location or Input/Output (I/O) port. Read (\overline{RD}) and Chip Select (\overline{CS}) inputs control three-state outputs. Two interface modes ensure compatibility with most popular μ Ps. The MAX151 is available in both 0.3" DIP and Wide SO packages.



II. Manufacturing Information

Α.	Description/Function:
	B 000011p 1011/1 011011011

- B. Process:
- C. Number of Device Transistors:
- D. Fabrication Location:
- E. Assembly Location:
- F. Date of Initial Production:

III. Packaging Information

A. Package Type:	24-pin PDIP
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive Epoxy
E. Bondwire:	Au (1.3 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	75°C/W
K. Single Layer Theta Jc:	30°C/W

IV. Die Information

A. Dimensions:	149 X 207 mils
B. Passivation:	$Si_3N_4\!/SiO_2$ (Silicon nitride/ Silicon dioxide
C. Interconnect:	Aluminum/Si (Si = 1%)
D. Backside Metallization:	None
E. Minimum Metal Width:	5.0 microns (as drawn)
F. Minimum Metal Spacing:	5.0 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw

300kHz, 10-Bit ADC with Reference and Track/Hold

SG5

Oregon

Pre 1997

Carsem Malaysia



V. Quality Assurance Information

Α.	Quality Assurance Contacts:	Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA)
B.	Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C.	Observed Outgoing Defect Rate:	< 50 ppm
D.	Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

 $\lambda = \underbrace{1}_{\text{MTTF}} = \underbrace{1.83}_{192 \times 4340 \times 190 \times 2}$ (Chi square value for MTTF upper limit) (where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV) $\lambda = 5.7 \times 10^{-9}$

𝔅 = 5.7 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the SG5 Process results in a FIT Rate of 0.4 @ 25C and 7.4 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The AD24 die type has been found to have all pins able to withstand a HBM transient pulse of +/-3000V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-100 mA.



Table 1 Reliability Evaluation Test Results

MAX151BCNG+

TEST ITEM	TEST CONDITION		SAMPLE SIZE			
		IDENTIFICATION				
Static Life Test (Note 1)						
	Ta = 135°C	DC Parameters	190	0		
	Biased	& functionality				
	Time = 192 hrs.					
Moisture Testing (Note 2)						
85/85	Ta = 85°C	DC Parameters	77	0		
	RH = 85%	& functionality				
	Biased					
	Time = 1000hrs.					
Mechanical Stress (Note 2)						
Temperature	-65°C/150°C	DC Parameters	77	0		
Cycle	1000 Cycles	& functionality				
	Method 1010					

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data