

RELIABILITY REPORT

FOR

MAX3180EEUK+ (MAX3180E-MAX3183E)

PLASTIC ENCAPSULATED DEVICES

April 1, 2009

# **MAXIM INTEGRATED PRODUCTS**

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Approved by
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Quality Assurance
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### Conclusion

The MAX3180EEUK+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim"s continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim"s quality and reliability standards.

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## I. Device Description

#### A. General

The MAX3180E-MAX3183E single RS-232 receivers in a SOT23-5 package are designed for space- and cost-constrained applications requiring minimal RS-232 communications. The receiver inputs are protected to ±15kV using IEC 1000-4-2 Air-Gap Discharge, to ±8kV using IEC 1000-4-2 Contact Discharge, and to ±15kV per the Human Body Model, ensuring compliance with international standards. The devices minimize power and heat dissipation by consuming only 0.5µA supply current from a +3.0V to +5.5V supply, and they guarantee true RS-232 performance up to a 1.5Mbps data rate. The MAX3180E/MAX3182E feature a three-state TTL/CMOS receiver output that is controlled by an active-low EN logic input. The MAX3181E/MAX3183E feature an active-low INVALID output that indicates valid RS-232 signals at the receiver input for applications requiring automatic system wake-up. The MAX3182E/MAX3183E have a noninverting output, while the MAX3180E/MAX3181E have a standard inverting output.



## II. Manufacturing Information

A. Description/Function: ±15kV ESD-Protected, 0.5μA, +3V to +5.5V, 1.5Mbps RS-232 Receivers in

SOT23-5

B. Process: M5

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: Carsem Malaysia, ISPL Philippines, Hana Thailand, UTL Thailand, Unisem

Malaysia

F. Date of Initial Production: April 24, 1999

### III. Packaging Information

A. Package Type: 5-pin SOT23
B. Lead Frame: Copper

C. Lead Finish: 100% matte Tin
D. Die Attach: Conductive Epoxy
E. Bondwire: Gold (1 mil dia.)
F. Mold Material: Epoxy with silica filler
G. Assembly Diagram: #05-1901-0200
H. Flammability Rating: Class UL94-V0

I. Classification of Moisture Sensitivity per

JEDEC standard J-STD-020-C

Level 1

J. Single Layer Theta Ja: 324.3°C/WK. Single Layer Theta Jc: 82°C/W

### IV. Die Information

A. Dimensions: 57 X 38 mils

B. Passivation: Si<sub>3</sub>N<sub>4</sub>/SiO<sub>2</sub> (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn)
 F. Minimum Metal Spacing: Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
H. Isolation Dielectric: SiO<sub>2</sub>
I. Die Separation Method: Wafer Saw



### V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm</li>D. Sampling Plan: Mil-Std-105D

### VI. Reliability Evaluation

#### A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$x = 4.7 \times 10^{-9}$$

% = 4.7 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maximic.com/. Current monitor data for the M5 Process results in a FIT Rate of 3.2 @ 25C and 54.8 @ 55C (0.8 eV, 60% UCL)

#### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

### C. E.S.D. and Latch-Up Testing

The RS78 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250 mA.



## Table 1

# Reliability Evaluation Test Results

# MAX3180EEUK+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (	(Note 1)				
	Ta = 135°C Biased	DC Parameters & functionality	230	0	
	Time = 192 hrs.	a ranononanty			
Moisture Testing	(Note 2)				
85/85	$Ta = 85^{\circ}C$	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 1000hrs.				
Mechanical Stres	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010				

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data