RELIABILITY REPORT

FOR

MAX6705SKA+T

PLASTIC ENCAPSULATED DEVICES

September 9, 2011

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.

SUNNYVALE, CA 94086

Approved by
Sokhom Chum
Quality Assurance
Reliability Engineer

Conclusion

The MAX6705SKA+T successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX6701-MAX6708 microprocessor (µP) supervisory circuits reduce the complexity and components required to monitor power-supply functions in µP systems. These devices significantly improve system reliability and accuracy compared to separate ICs or discrete components. The MAX6701-MAX6708 family provides four functions: a reset output during power-up, power-down, and brownout conditions; an independent watchdog output that goes low if the watchdog input has not been toggled within 1.6s; a 0.62V threshold detector for power-fail warning; and an active-low manual reset input. The MAX6701-MAX6708 family offers several pinout options to accommodate a variety of multivoltage microprocessor supervision applications. The MAX6701(A)/MAX6702(A)/MAX6703(A) monitor three supply voltages (one fixed threshold and two adjustable) to drive a single reset output and include a manual reset input and a watchdog timer with an independent output. The MAX6704 monitors a single-supply voltage to drive complementary reset outputs and includes an independent adjustable power-fail-in/power-fail-out comparator, a manual reset input, and a reset-based watchdog timer. The MAX6705(A)/MAX6706(A)/MAX6707(A) monitor a single-supply voltage to drive a single reset output and include an independent adjustable power-fail-in/power-fail-out comparator, a manual reset input, and a watchdog timer with an independent output. The MAX6708 is the same as the MAX6704 but without the watchdog timer function. *See the Detailed Description for differences between non-A and A versions.*

II. Manufacturing Information

A. Description/Function:	Low-Voltage, SOT23, µP Supervisors with Power-Fail In/Out, Manual Reset, and Watchdog Timer
B. Process:	B8
C. Number of Device Transistors:	
D. Fabrication Location:	Oregon
E. Assembly Location:	Thailand
F. Date of Initial Production:	January 27, 2001

III. Packaging Information

A. Package Type:	8L SOT23
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive
E. Bondwire:	Au (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-1601-0136 / A
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	1
J. Single Layer Theta Ja:	N/A
K. Single Layer Theta Jc:	N/A
L. Multi Layer Theta Ja:	194°C/W
M. Multi Layer Theta Jc:	70°C/W

IV. Die Information

A. Dimensions:	62 X 24 mils
B. Passivation:	Si_3N_4/SiO_2 (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Al/0.5%Cu with Ti/TiN Barrier
D. Backside Metallization:	None
E. Minimum Metal Width:	0.8 microns (as drawn)
F. Minimum Metal Spacing:	0.8 microns (as drawn)
G. Bondpad Dimensions:	
H. Isolation Dielectric:	SiO_2
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

A. Quality Assurance Contacts:	Richard Aburano (Manager, Reliability Engineering) Don Lipps (Manager, Reliability Engineering) Bryan Preeshl (Vice President of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{MTTF} = \frac{1.83}{192 \times 4340 \times 80 \times 2}$$ (Chi square value for MTTF upper limit)

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$\lambda = 13.7 \times 10^{-9}$

λ = 13.7 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor. Cumulative monitor data for the B8 Process results in a FIT Rate of 0.06 @ 25C and 0.99 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (ESD lot I0T8AA006A D/C 0246, Latch-Up lot S0T8BA015A D/C 0518)

The MS51 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1500V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250mA.

Table 1
Reliability Evaluation Test Results

MAX6705SKA+T

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (Note 1)					
	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	80	0	I0T0AQ001B, D/C 0052

Note 1: Life Test Data may represent plastic DIP qualification lots