



RELIABILITY REPORT
FOR
MAX843ISA+
PLASTIC ENCAPSULATED DEVICES

June 16, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.
SUNNYVALE, CA 94086

Approved by
Ken Wendel
Quality Assurance
Director, Reliability Engineering

Conclusion

The MAX843ISA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

Table of Contents

I.Device Description	V.Quality Assurance Information
II.Manufacturing Information	VI.Reliability Evaluation
III.Packaging Information	IV.Die Information
.....Attachments	

I. Device Description

A. General

The MAX840/MAX843/MAX844 low-noise, inverting charge-pump power supplies are ideal for biasing GaAsFETs in cellular telephone transmitter amplifiers. They operate with inputs down to 2.5V. The MAX840 offers both a -2V preset output and a -0.5V to -9.4V adjustable output. The MAX843/MAX844 use an external positive control voltage to set the negative output voltage. Input voltage range for all the devices is 2.5V to 10V, and output current is 4mA with $V_{IN} > 2.7V$. These circuits can operate with small capacitors, as low as 0.22 μF . An internal linear regulator reduces the MAX840's output voltage ripple to 1mVp-p. With a well-filtered control voltage (VCTRL), the MAX843/MAX844 also achieve less than 1mVp-p typical output ripple. Supply current is 750 μA , and reduces to less than 1 μA in shutdown (MAX840/MAX843). The MAX844's unregulated output is active in shutdown, with the charge pump switching at 20kHz. It provides a low-power LCD supply.

II. Manufacturing Information

A. Description/Function:	Low-Noise, Regulated, -2V GaAsFET Bias
B. Process:	M5
C. Number of Device Transistors:	
D. Fabrication Location:	Oregon
E. Assembly Location:	Philippines, Thailand
F. Date of Initial Production:	Pre 1997

III. Packaging Information

A. Package Type:	8-pin SOIC (N)
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive Epoxy
E. Bondwire:	Gold (1.3 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-1701-0243
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	170°C/W
K. Single Layer Theta Jc:	40°C/W
L. Multi Layer Theta Ja:	128.4°C/W
M. Multi Layer Theta Jc:	36°C/W

IV. Die Information

A. Dimensions:	85 X 145 mils
B. Passivation:	Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Al/0.5% cu
D. Backside Metallization:	None
E. Minimum Metal Width:	Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn)
F. Minimum Metal Spacing:	Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

A. Quality Assurance Contacts:	Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 720 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 1.5 \times 10^{-9}$$

$$\lambda = 1.5 \text{ F.I.T. (60\% confidence level @ 25°C)}$$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at <http://www.maxim-ic.com/>. Current monitor data for the M5 Process results in a FIT Rate of 3.2 @ 25C and 54.8 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The PW69-3 die type has been found to have all pins able to withstand a HBM transient pulse of +/-3000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-100 mA.

Table 1
Reliability Evaluation Test Results

MAX843ISA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES
Static Life Test (Note 1)	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	720	0
Moisture Testing (Note 2) 85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality	77	0
Mechanical Stress (Note 2) Temperature Cycle	-65°C/150°C 1000 Cycles Method 1010	DC Parameters & functionality	77	0

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data