

Evaluating the ADXL359 Low Noise, Low Drift, Low Power 3-Axis MEMS Accelerometer

FEATURES

- ▶ 2 sets of spaced vias for population of 5-pin headers
- ▶ Easily attached to prototyping board or PCB
- ▶ Small size and board stiffness minimize impact on the system and acceleration measurement

EVALUATION KIT CONTENTS

- ▶ EVAL-ADXL359Z evaluation board
- ▶ 10-pin Harwin connector, M80-8541042

ONLINE RESOURCES

- ▶ [ADXL359](#) data sheet

GENERAL DESCRIPTION

The EVAL-ADXL359Z is a simple evaluation board that allows quick evaluation of the performance of the ADXL359, low noise, low power accelerometer. The EVAL-ADXL359Z is ideal for evaluation of the ADXL359 in an existing system because the stiffness and the small size of the EVAL-ADXL359Z minimizes the effect of the board on both the system and acceleration measurements.

The ADXL359 is a low noise density, low 0 g offset drift, low power, 3-axis microelectromechanical system (MEMS) accelerometer with selectable measurement range ($\pm 10\text{ g}$, $\pm 20\text{ g}$, and $\pm 40\text{ g}$). The ADXL359 offers industrial leading noise, minimal offset drift over temperature, and long-term stability, enabling precision applications with minimal calibration.

The low drift, low noise, and low power ADXL359 enables accurate tilt measurement in an environment with high vibration, such as airborne inertial measurement units (IMUs). The low noise over higher frequencies is ideal for wireless condition monitoring.

The ADXL359 multifunction pin names may be referenced only by their relevant function for either the serial peripheral interface (SPI) or limited I²C interface.

For full details on the ADXL359, see the ADXL359 data sheet, which should be consulted in conjunction with this user guide when using the EVAL-ADXL359Z evaluation board.

EVALUATION BOARD PHOTOGRAPHS

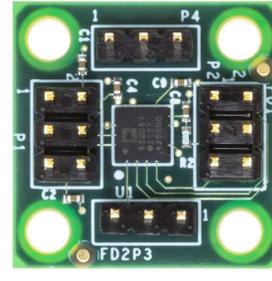


Figure 1. EVAL-ADXL359Z Evaluation Board Photograph (Top)

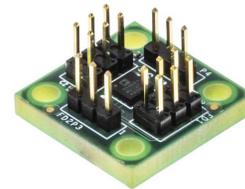


Figure 2. EVAL-ADXL359Z Evaluation Board Photograph (Angle)

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REVISION HISTORY**6/2022—Revision 0: Initial Version**

EVALUATION BOARD HARDWARE

The EVAL-ADXL359Z allows users to access the individual connections of the [ADXL359](#) and includes decoupling capacitors for the supplies, a few discrete resistors that provide isolation on the V_{1P8ANA} and V_{1P8DIG} pins, and two 6-pin headers. Refer to the ADXL359 data sheet for more details on the specific pin definitions. The power supplies for the ADXL359 are decoupled using multiple 0.1 μF ceramic (0603) capacitors.

The EVAL-ADXL359Z uses two 6-pin headers and two 3-pin headers to provide access to all pins. The P1 header provides access to VDDIO, VDD (which connects to the ADXL359 V_{SUPPLY} pin), V_{SS}/V_{SSIO} (supply common connection), INT1, INT2, and DRDY, as shown in [Figure 3](#).

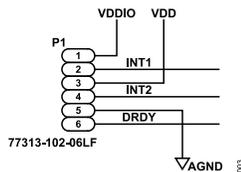


Figure 3. P1 Header Connections

The P2 header provides access to V_{1P8ANA} , SCLK/ V_{SSIO} , V_{1P8DIG} , MOIS/SDA, and MISO/ASEL, as shown in [Figure 4](#).

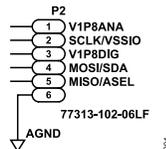


Figure 4. P2 Header Connections

The P3 header provides access to CS_ $_N$ /SCL, as shown in [Figure 5](#).

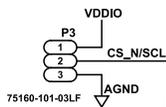


Figure 5. Header P3 CS_ $_N$ /SCL Access

The P4 header provides another connection to VDDIO and ground. The vias or headers allow the EVAL-ADXL359Z to attach to either a prototyping breadboard or a printed circuit board (PCB) in an existing user system. Four holes are provided in the corners of the EVAL-ADXL359Z for mechanical attachment of the EVAL-ADXL359Z in many applications. An external host processor is required for communication to the ADXL359.

The dimensions of the EVAL-ADXL359Z are 0.8 in \times 0.8 in.

EVALUATION BOARD CIRCUITRY

The ADXL359 has two power modes. The device can be powered either by integrated, low dropout (LDO) regulators or by external user supplied 1.8 V regulated supplies. Refer to the ADXL359 data sheet for more information.

HANDLING CONSIDERATIONS

The EVAL-ADXL359Z is not reverse polarity protected. Reversing of any of the supply connections, including the V_{SS} and V_{SSIO} pins, can cause damage to the ADXL359.

Dropping the EVAL-ADXL359Z on a hard surface can generate several thousand g of acceleration, which can exceed the ADXL359 data sheet absolute maximum limits.

EVALUATION BOARD SCHEMATIC

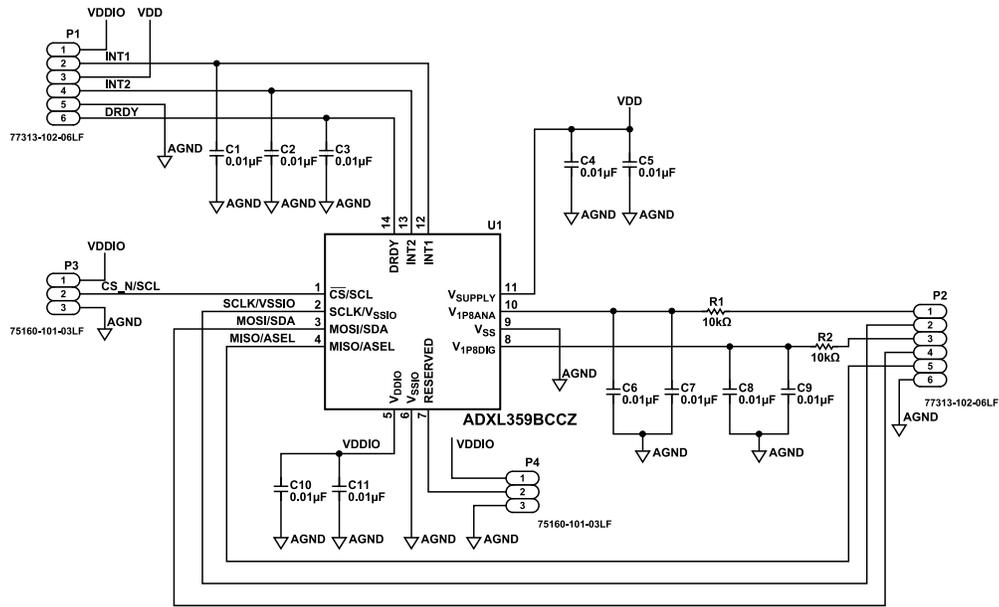


Figure 6. EVAL-ADXL359Z Evaluation Board Schematic

ORDERING INFORMATION

BILL OF MATERIALS

Table 1. Bill of Materials

Quantity	Reference Designator	Description	Manufacturer	Part Number
1	U1	Low noise, low drift, low power 3-axis MEMS accelerometer	Analog Devices, Inc.	ADXL359BCCZ
11	C1 to C11	0.01 μ F capacitors, 50 V, 10%, X7R, 0402, AEC-Q200	Murata	GCM155R71H103KA55D
2	P1, P2	Headers, male, unshrouded, double, 2.54 mm pitch, 5.84 mm mating length, 3.42 mm solder tail	Amphenol FCI	77313-102-06LF
2	P3, P4	Headers, male, unshrouded, single, row 0.62 mm square post header, 5.72 mm post height, 2.54 mm solder tail, 2.54 mm pitch	Amphenol	75160-101-03LF
2	R1, R2	10 k Ω resistors, 0.1%, 1/16 W, 0402	Vishay	MCS04020D1002BE100

¹²C refers to a communications protocol originally developed by Philips Semiconductors (now NXP Semiconductors).

**ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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